

FIG. 1

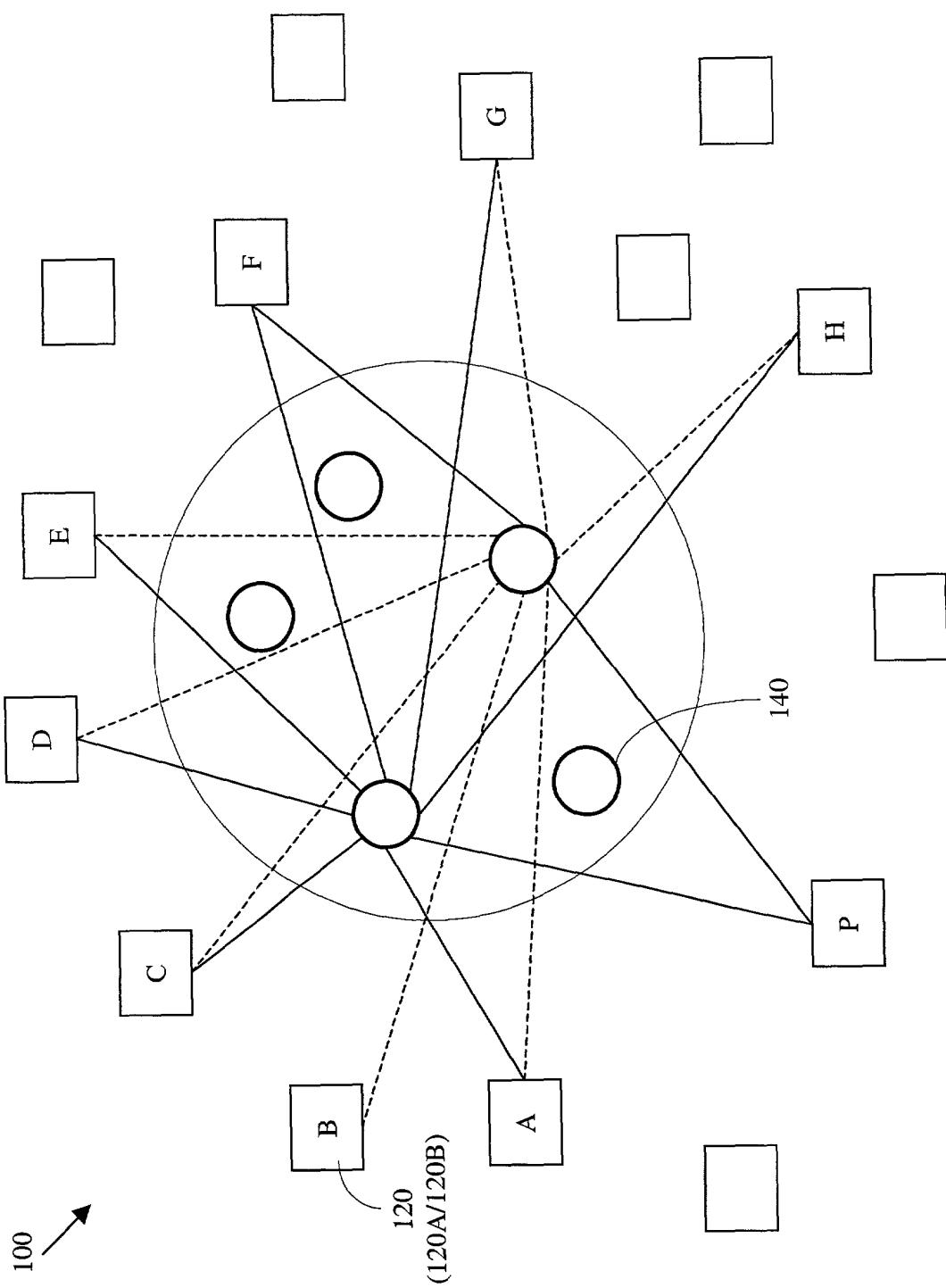
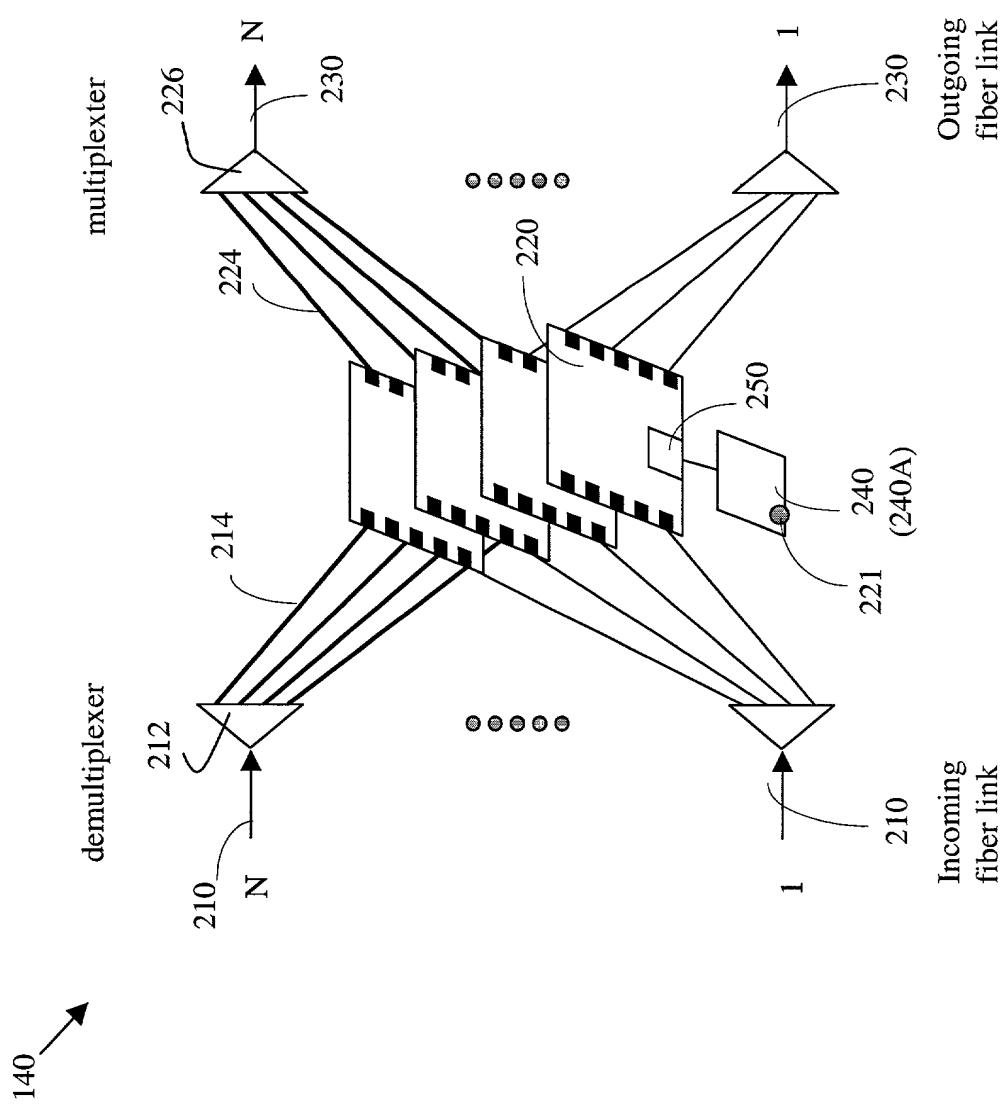


FIG. 2



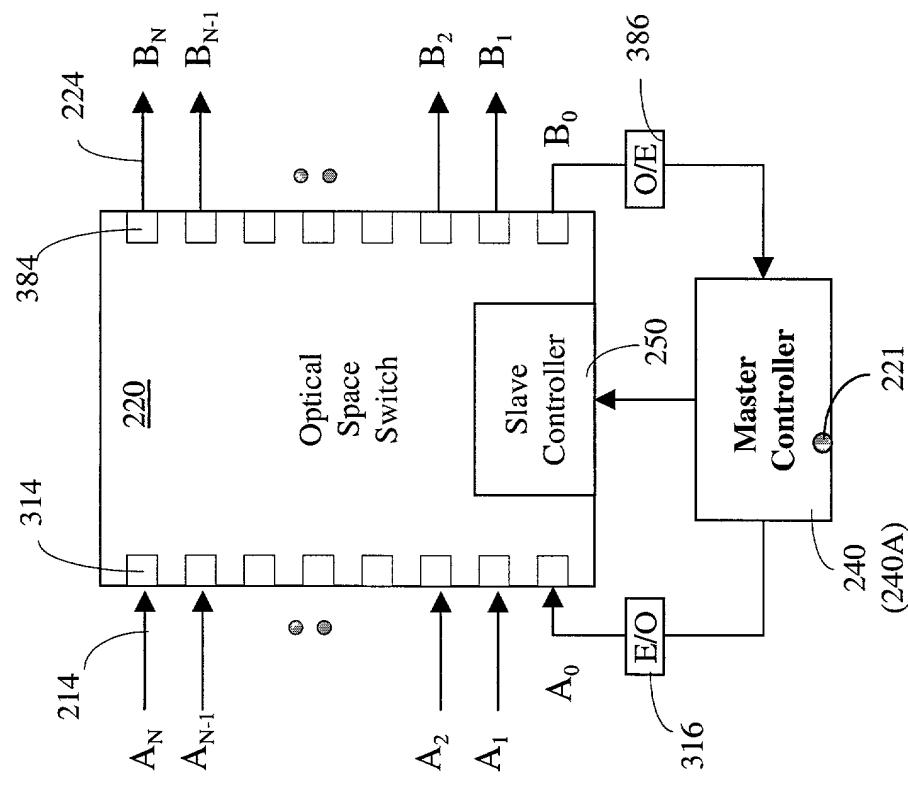


FIG. 3

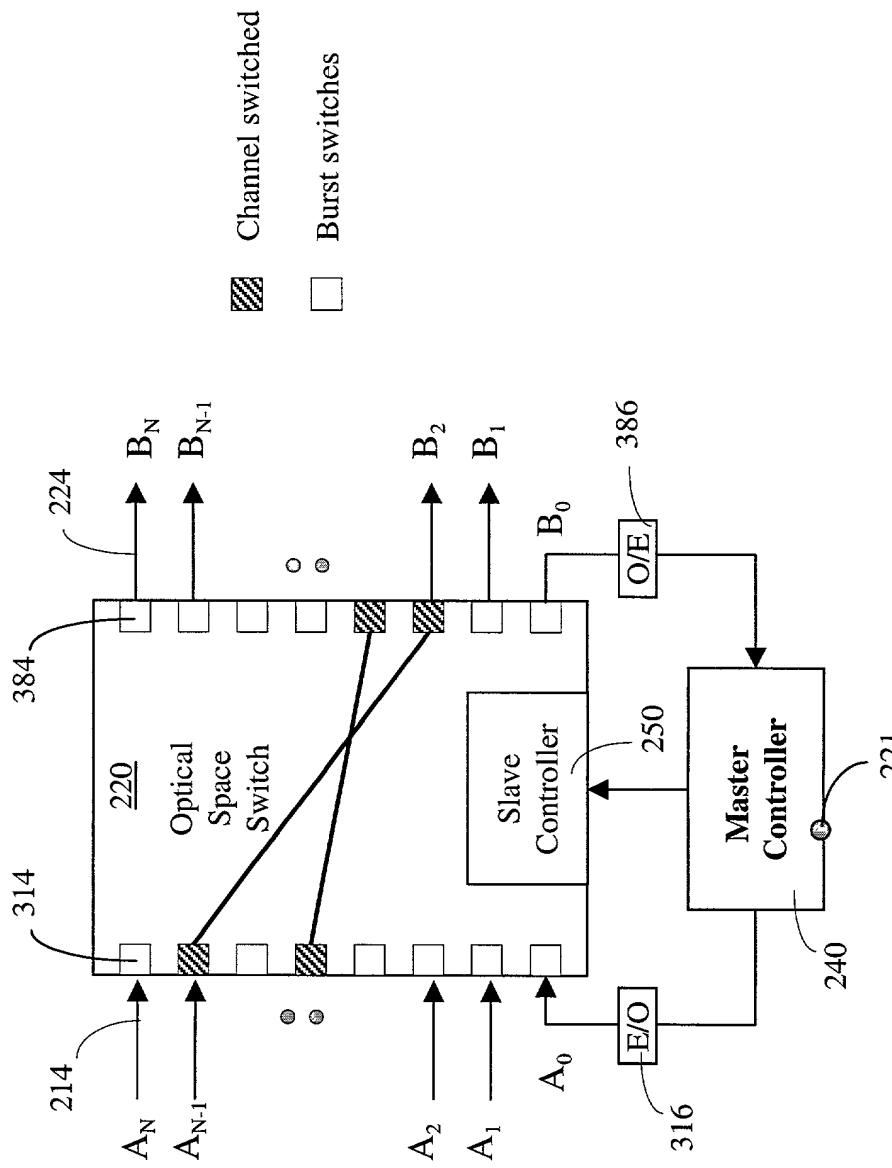


FIG. 4

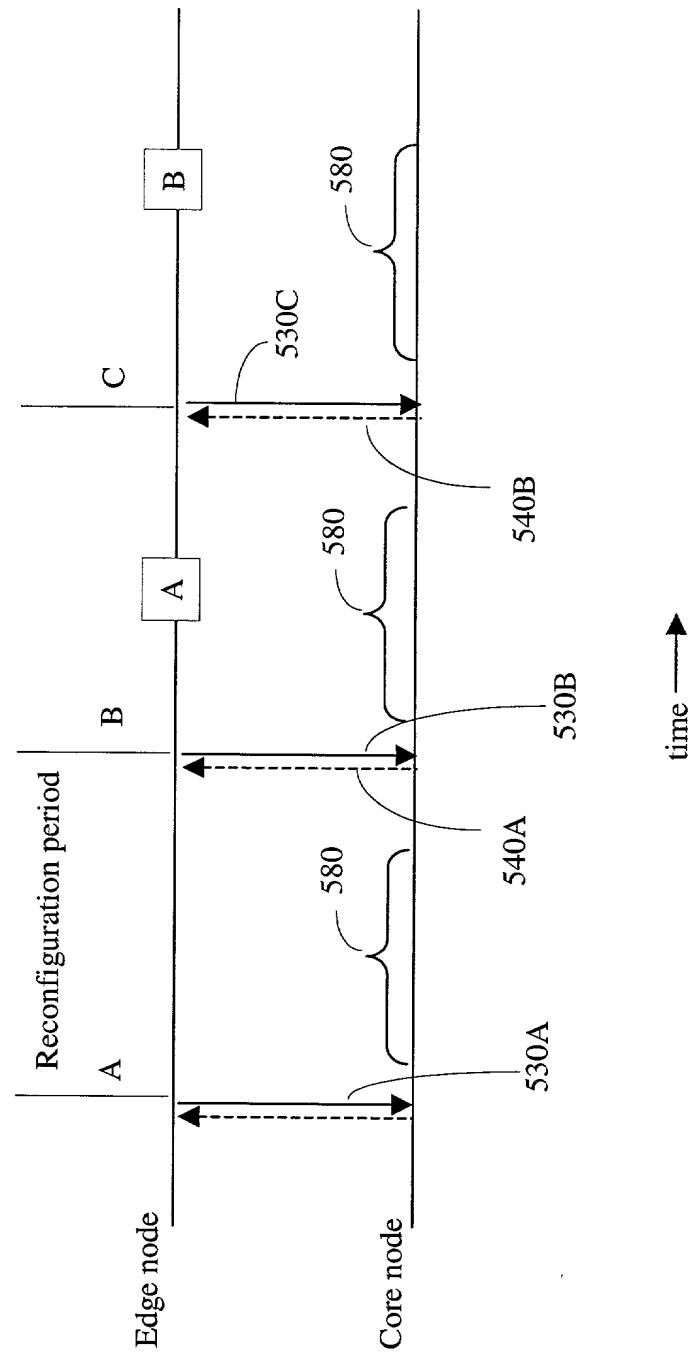
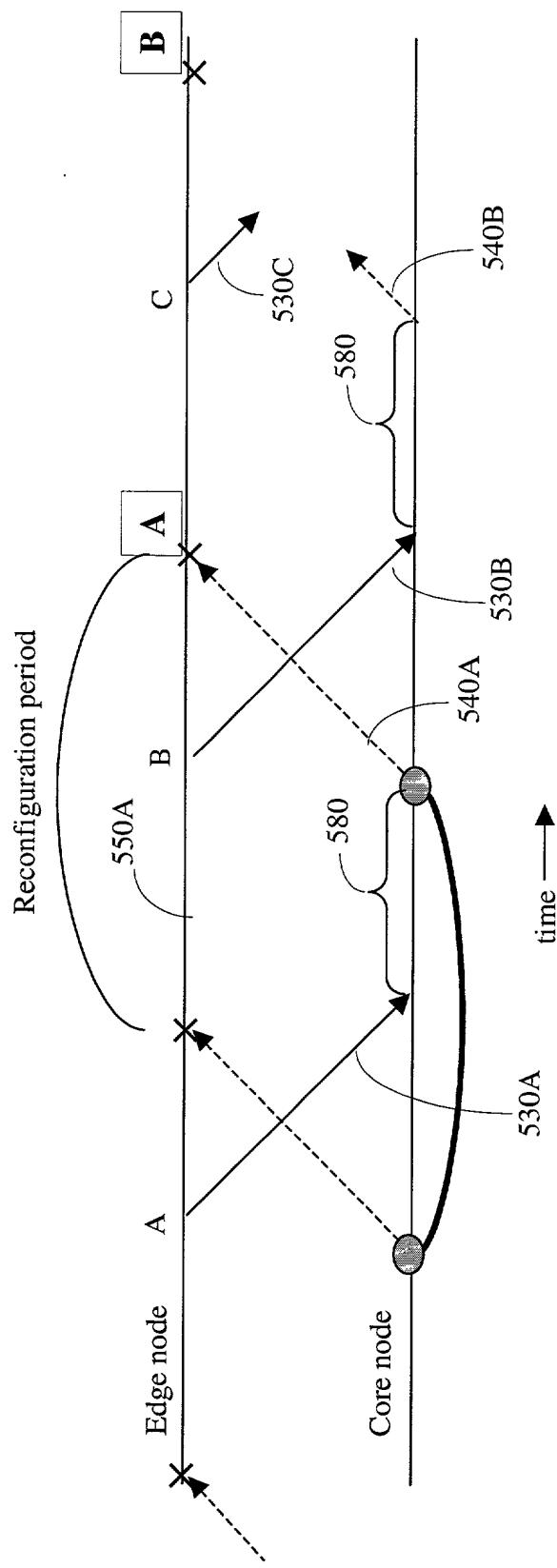


FIG. 5

FIG. 6



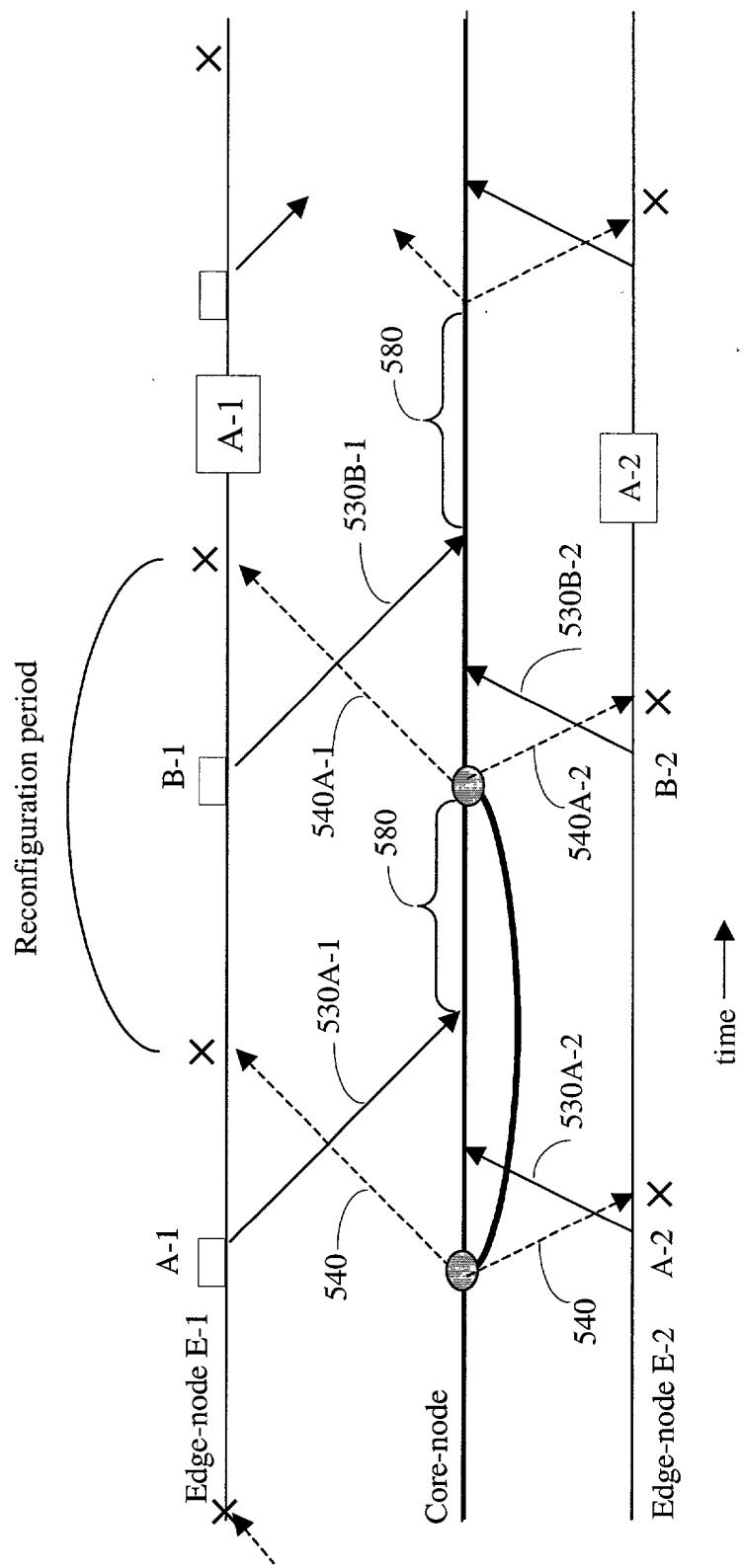
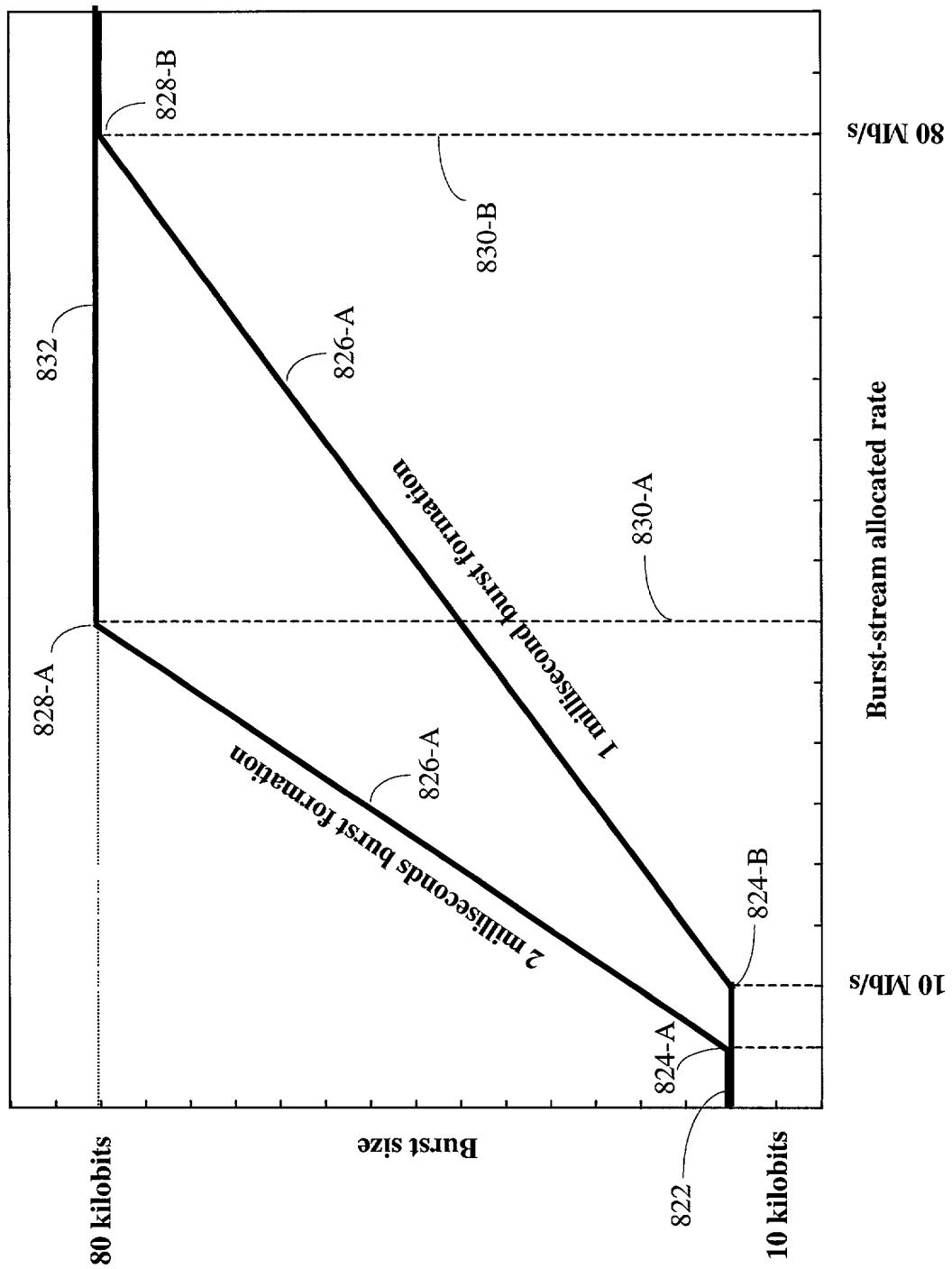


FIG. 7

FIG. 8



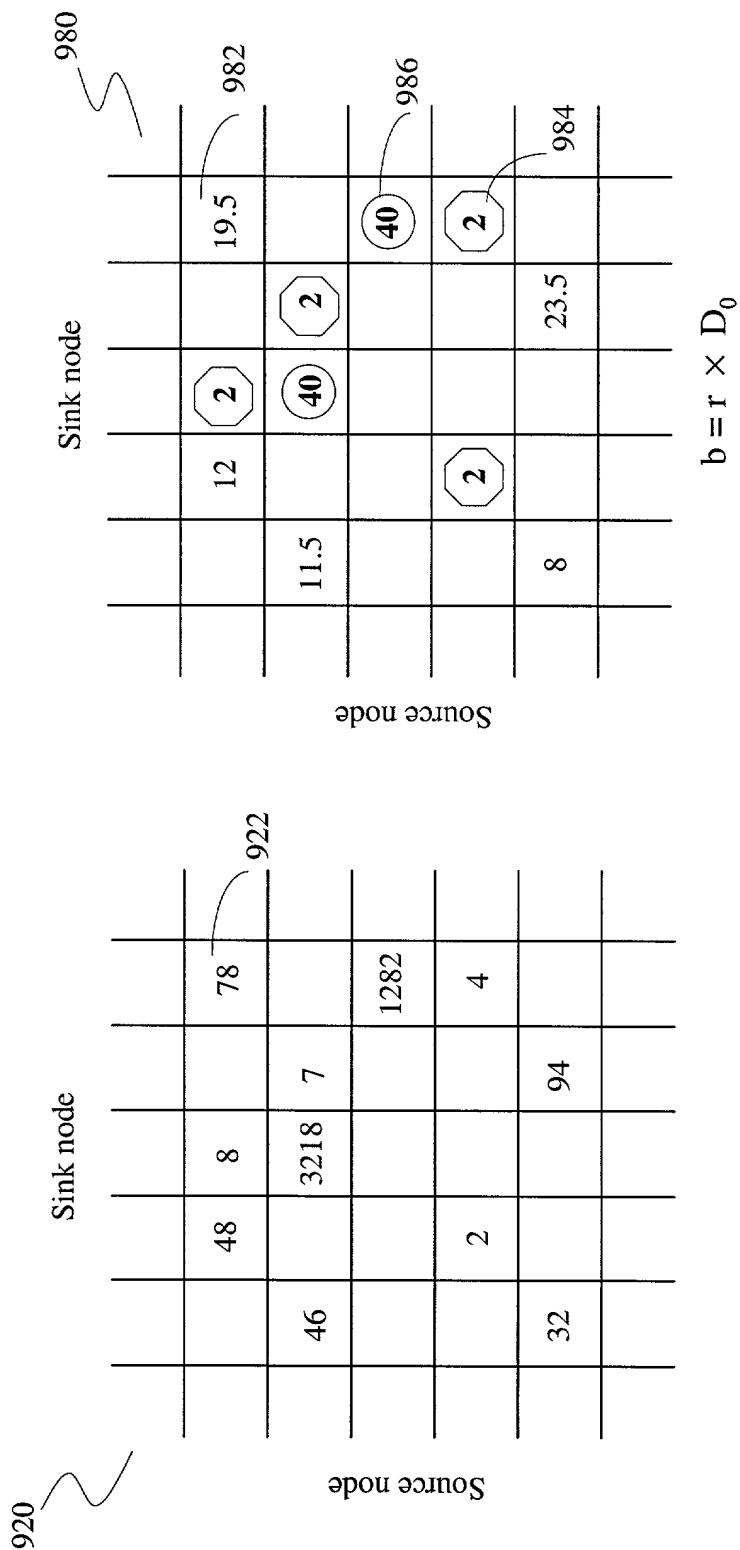


FIG. 9

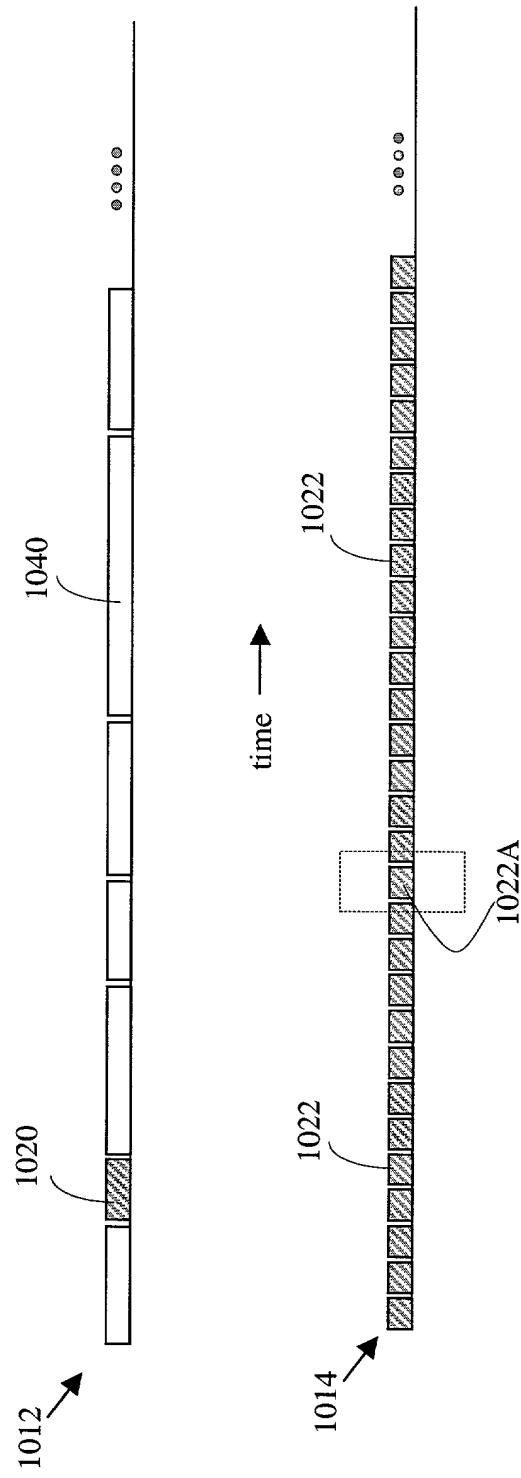


FIG. 10

FIG. 11

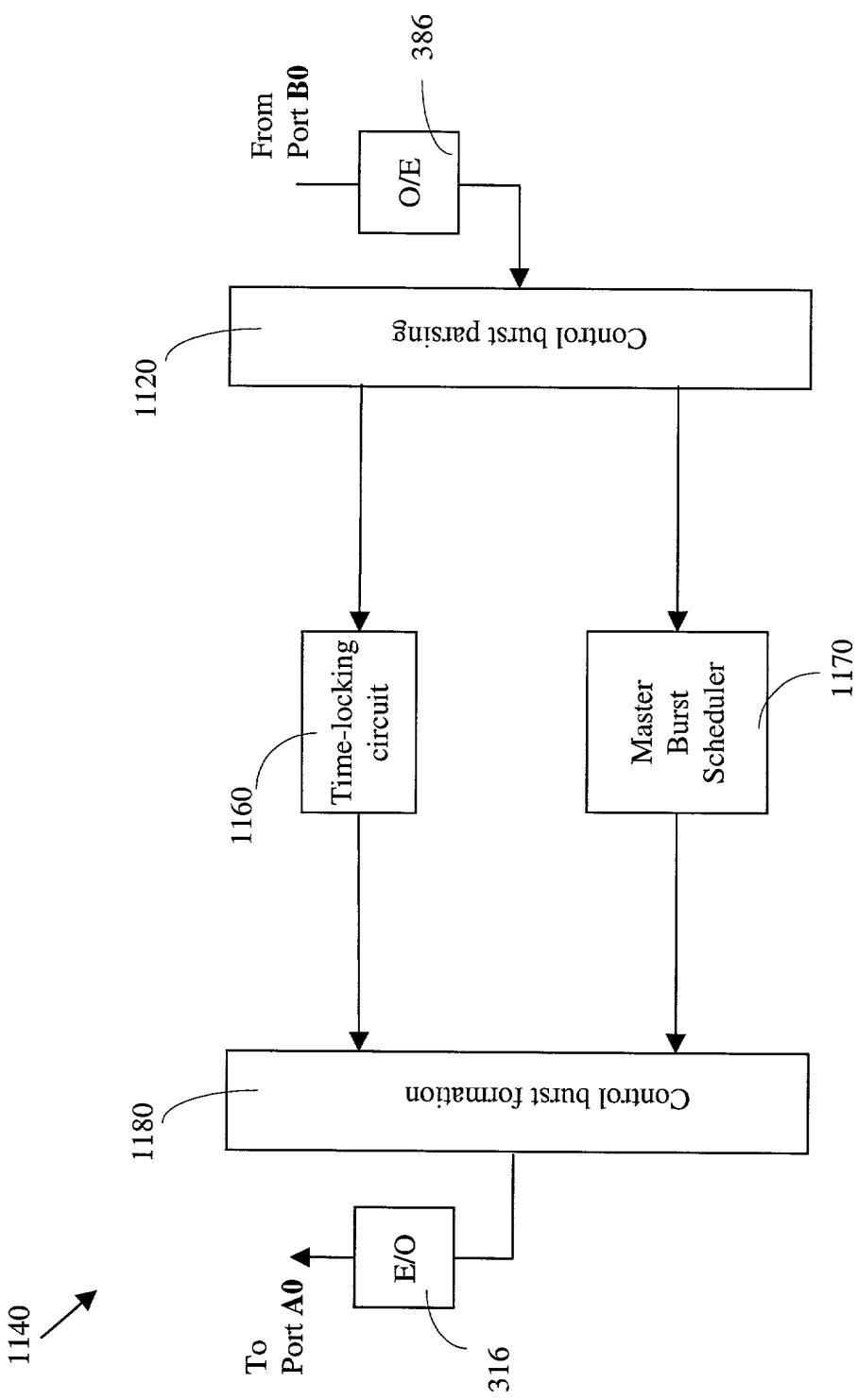
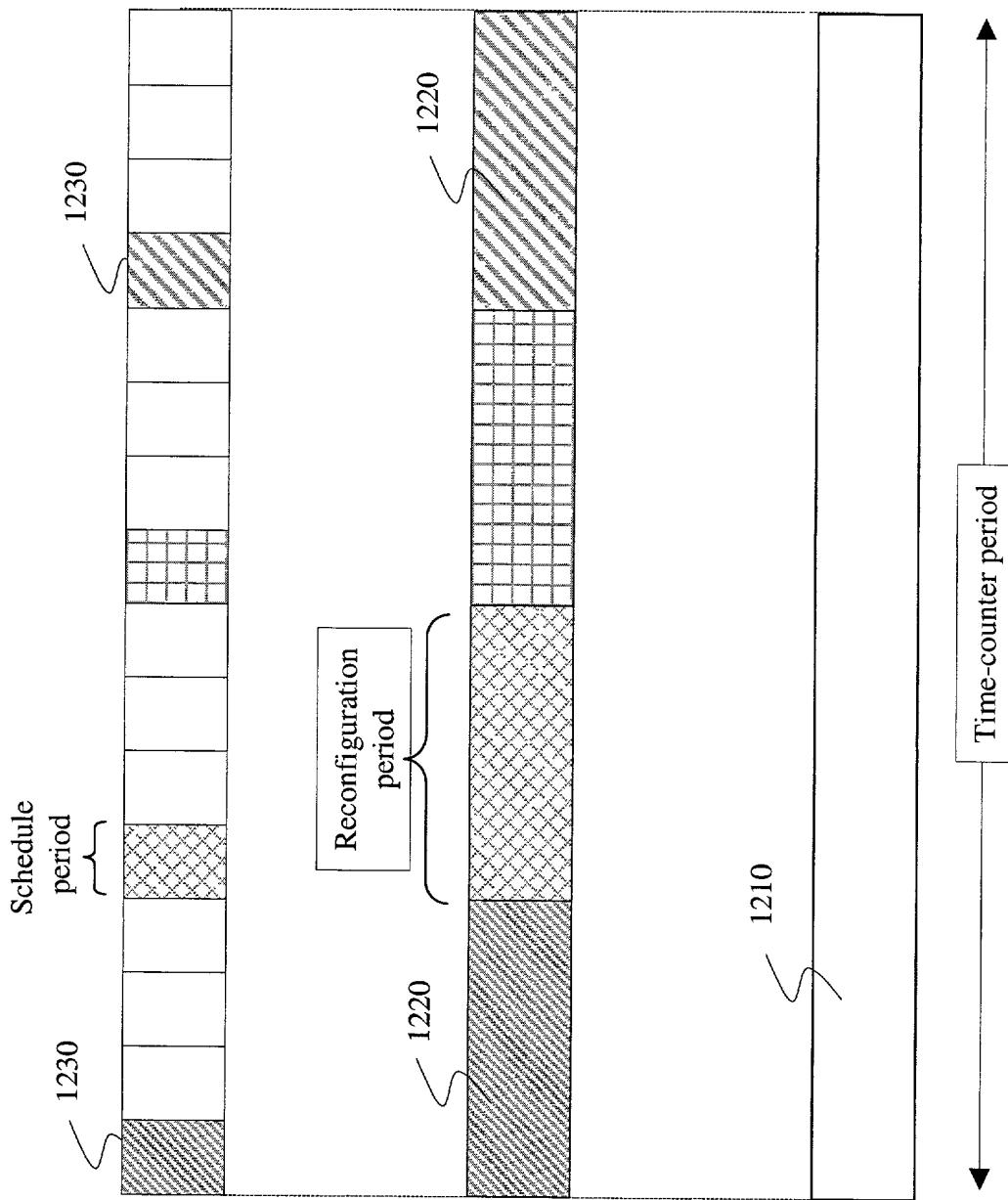


FIG. 12



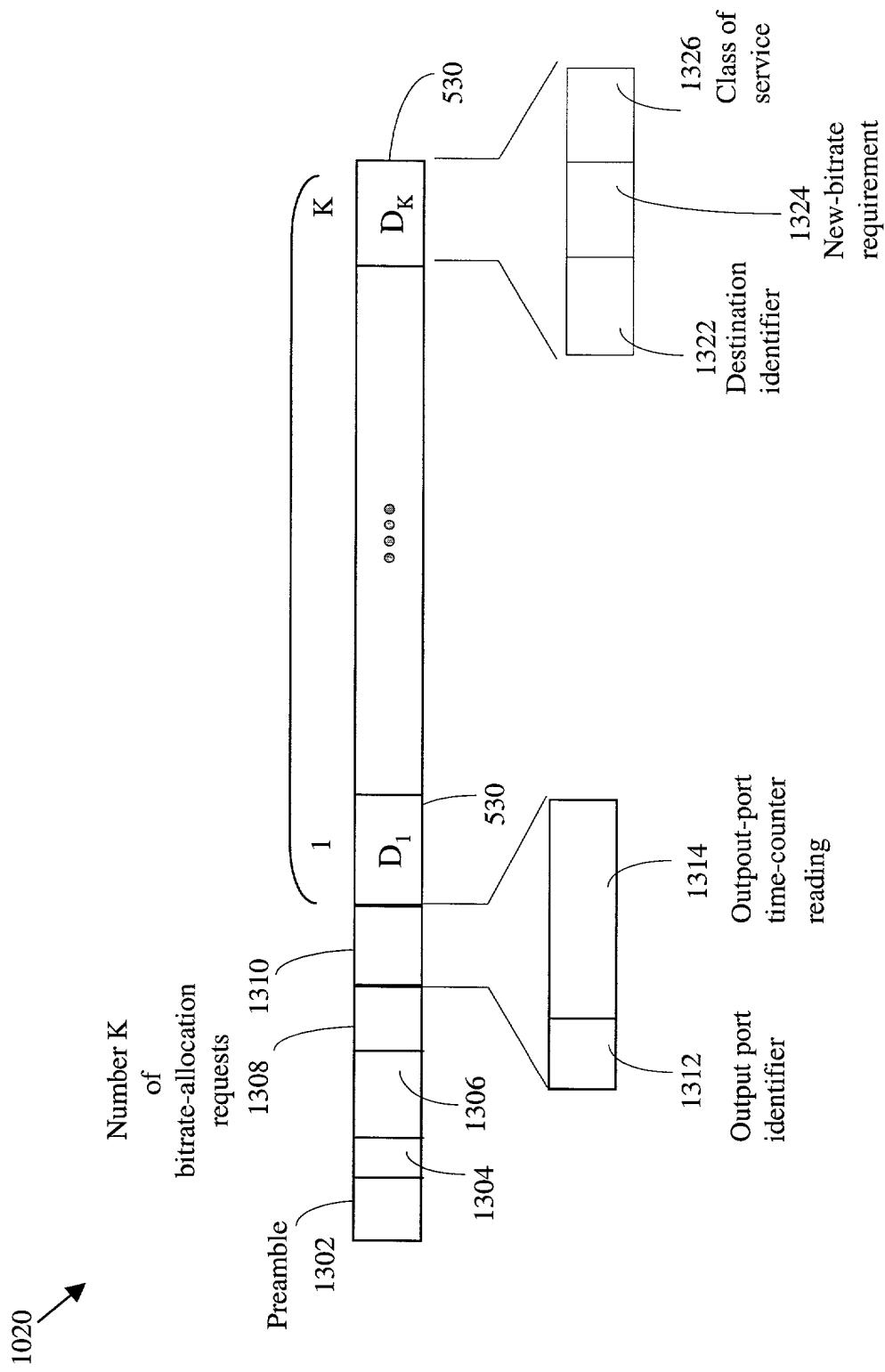


FIG. 13

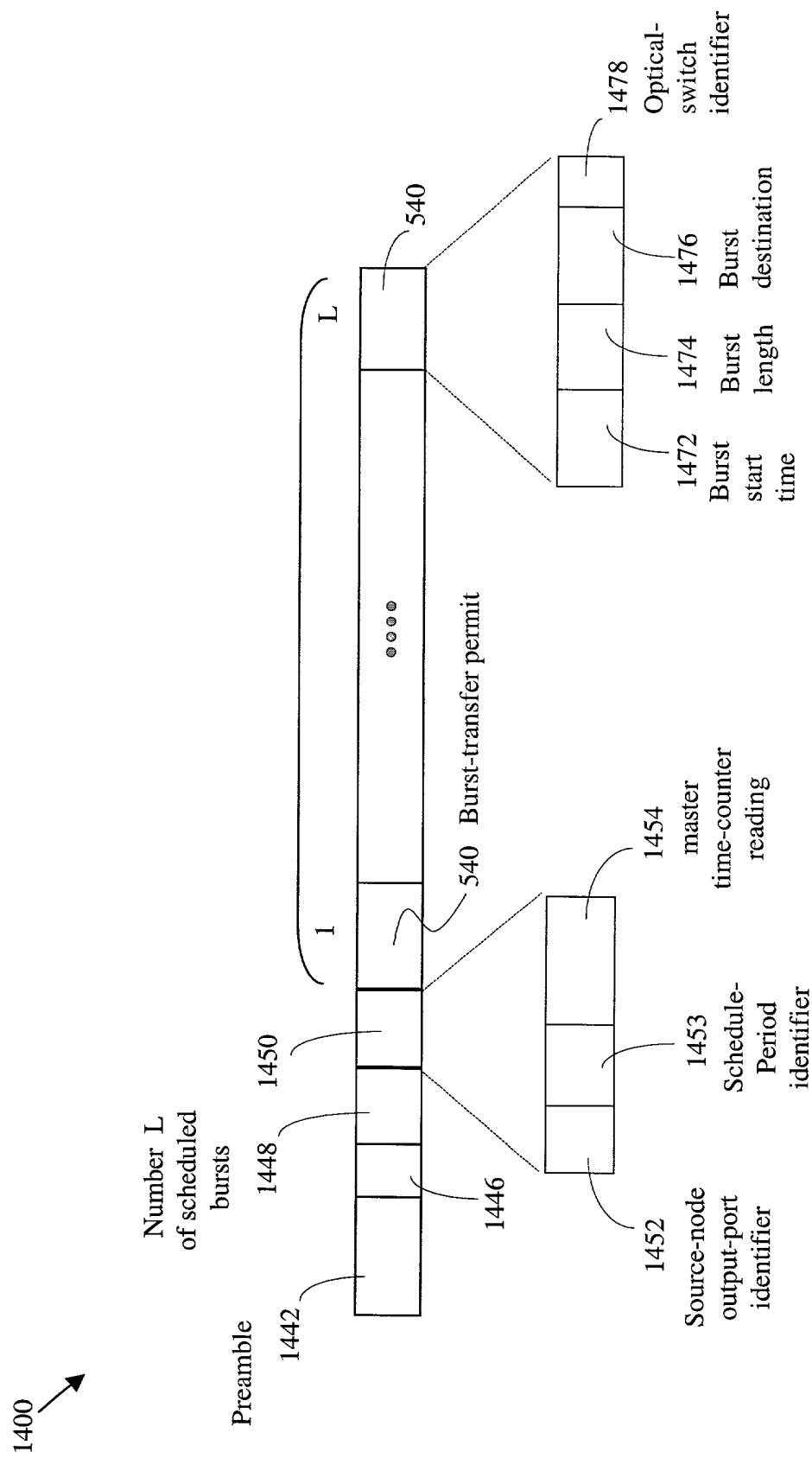


FIG. 14

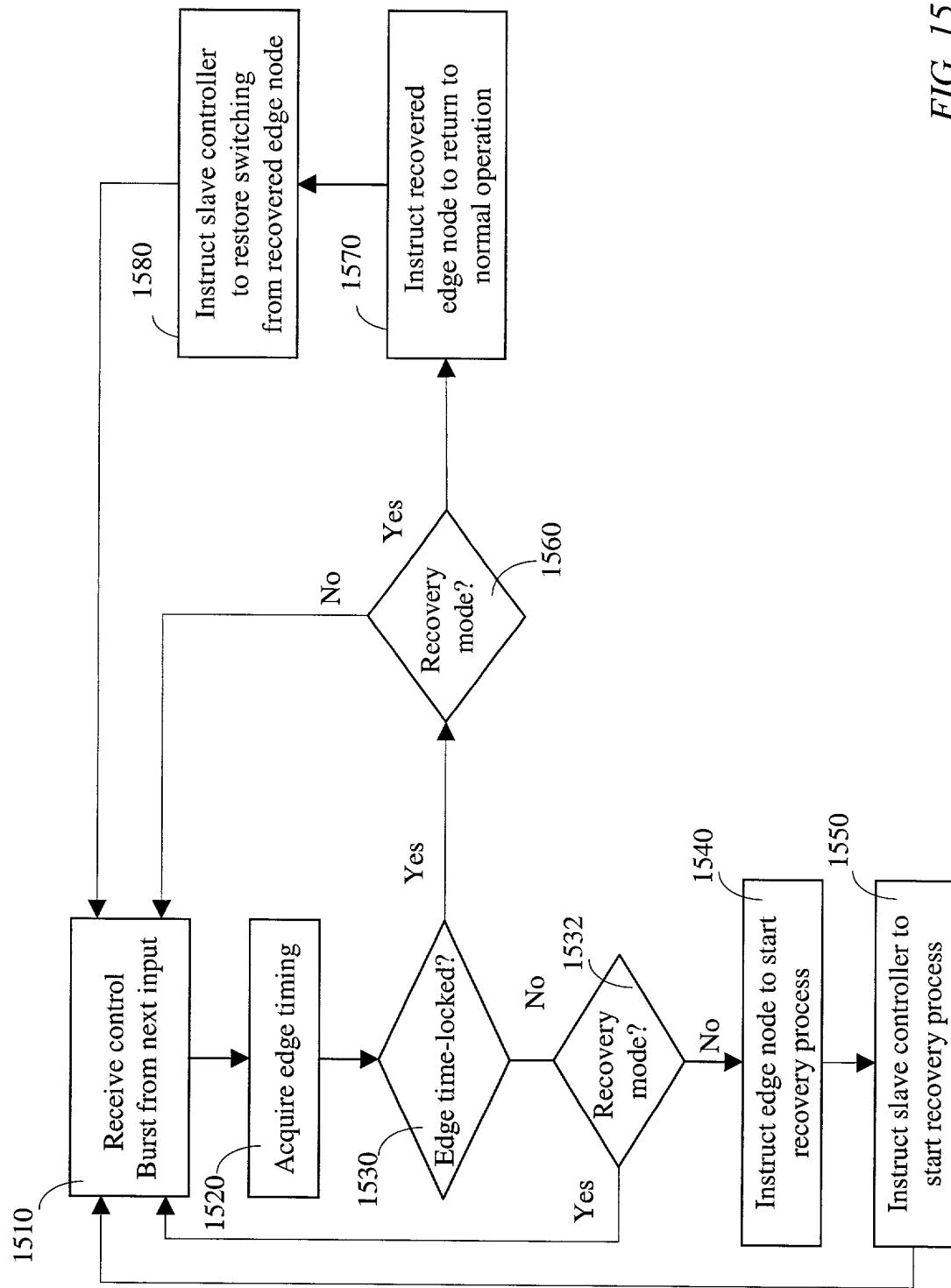
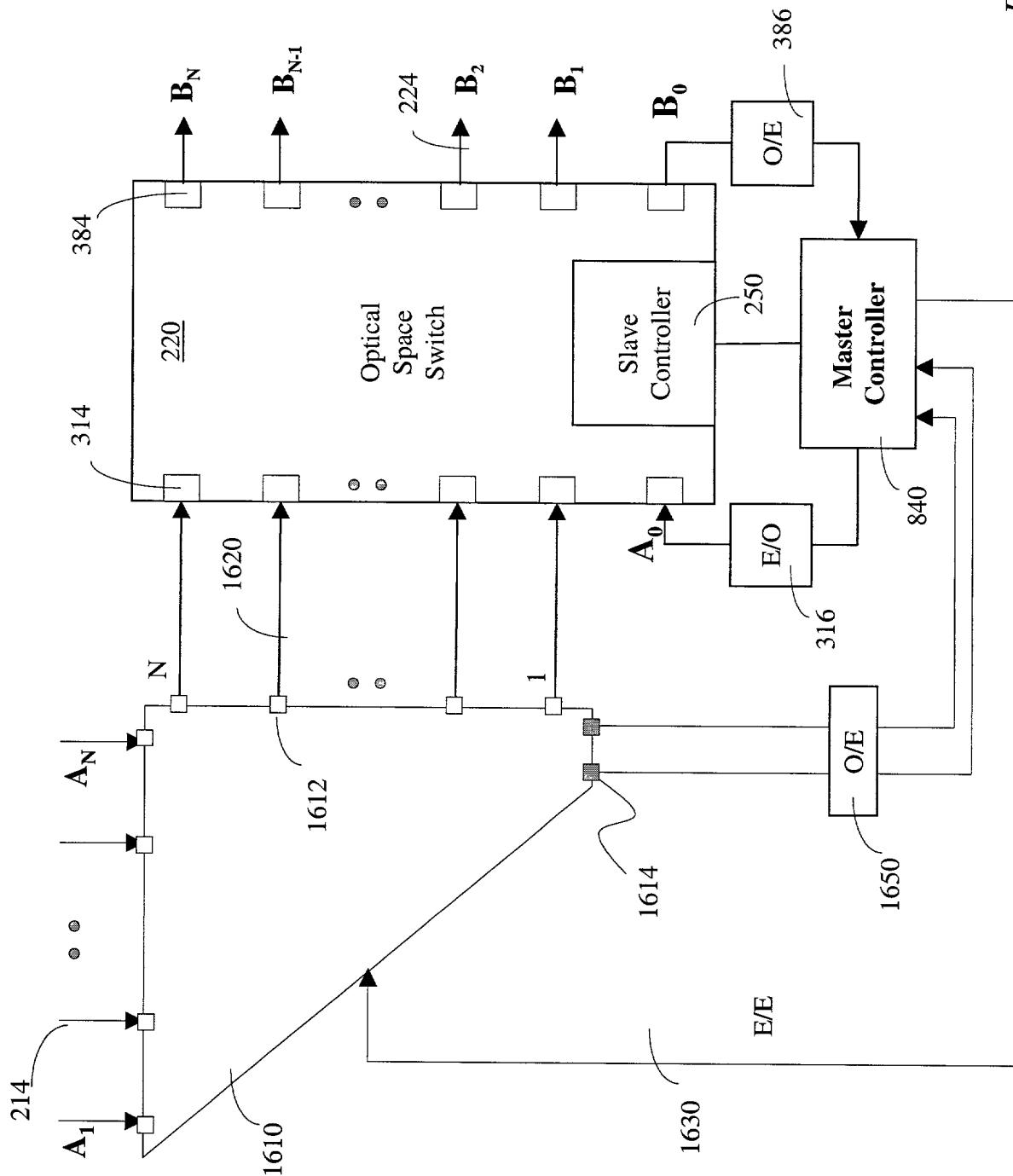


FIG. 15

FIG. 16



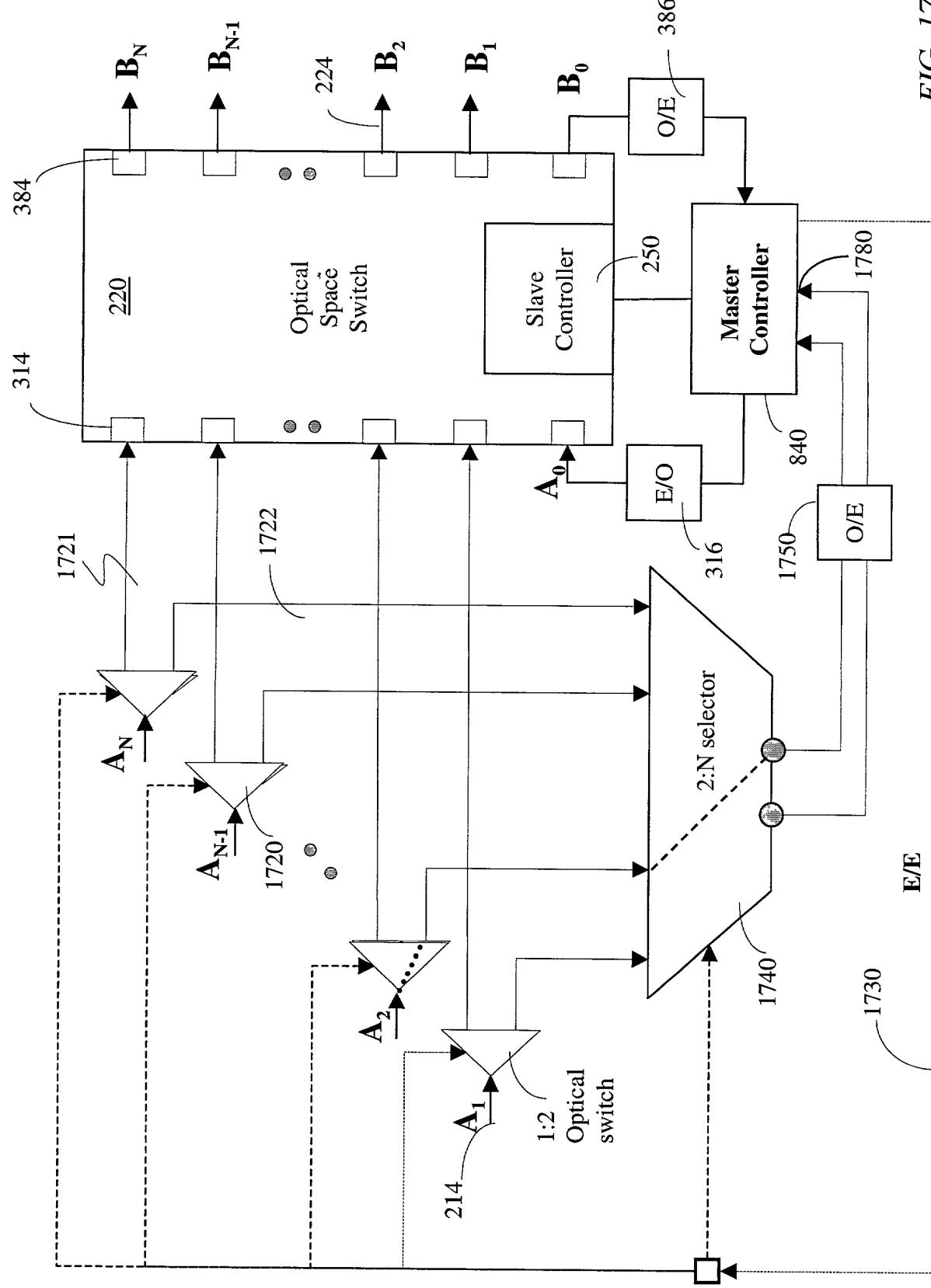


FIG. 17

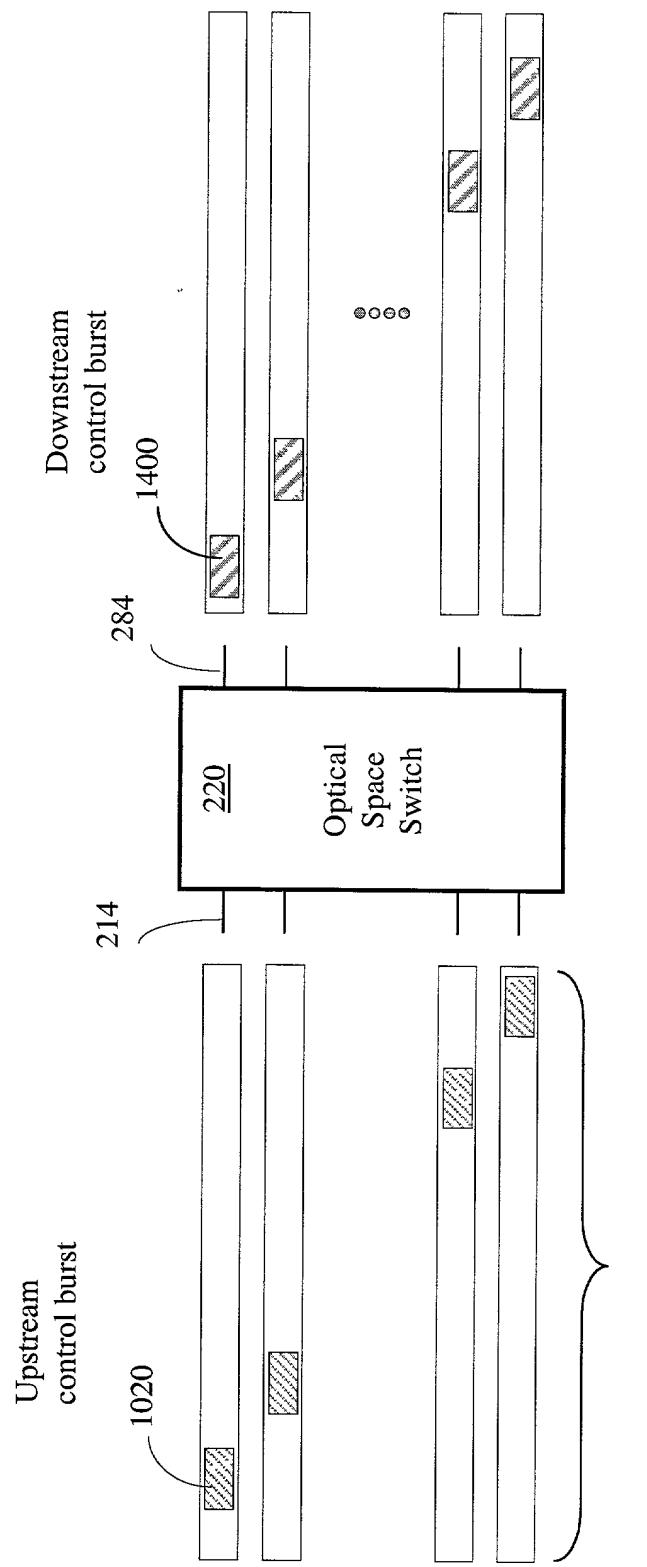


FIG. 18

Time-counter cycle (master cycle) Period: 2^W time slots

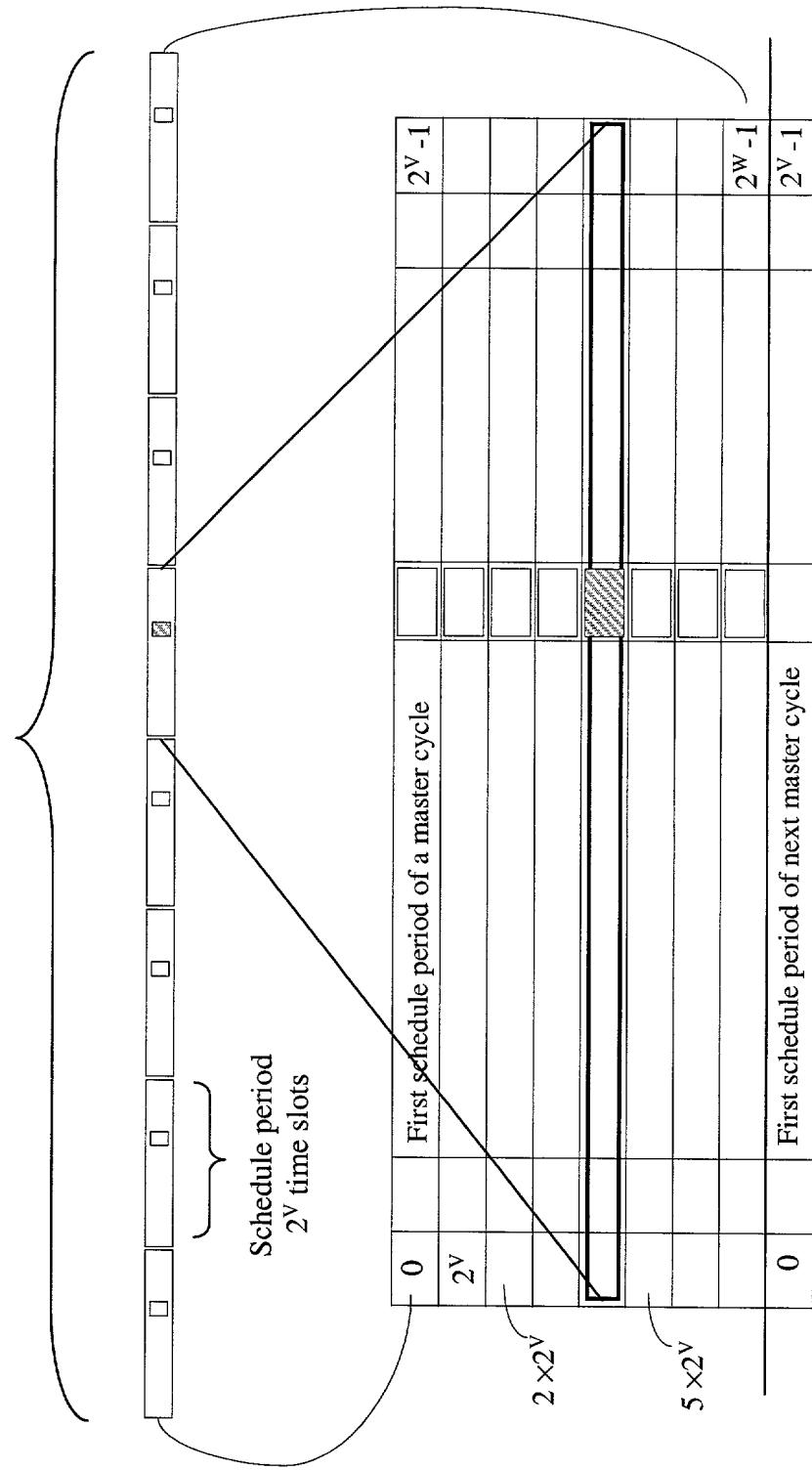


FIG. 19

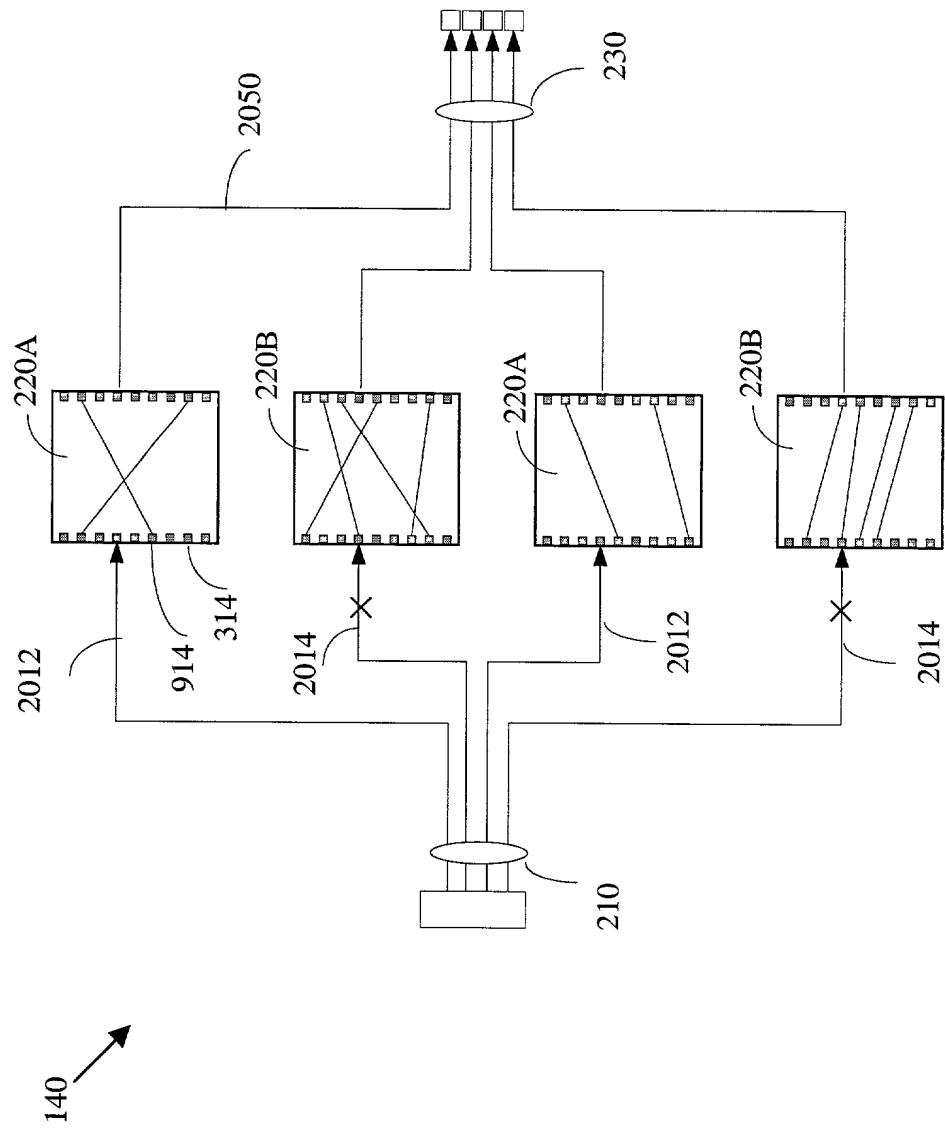


FIG. 20

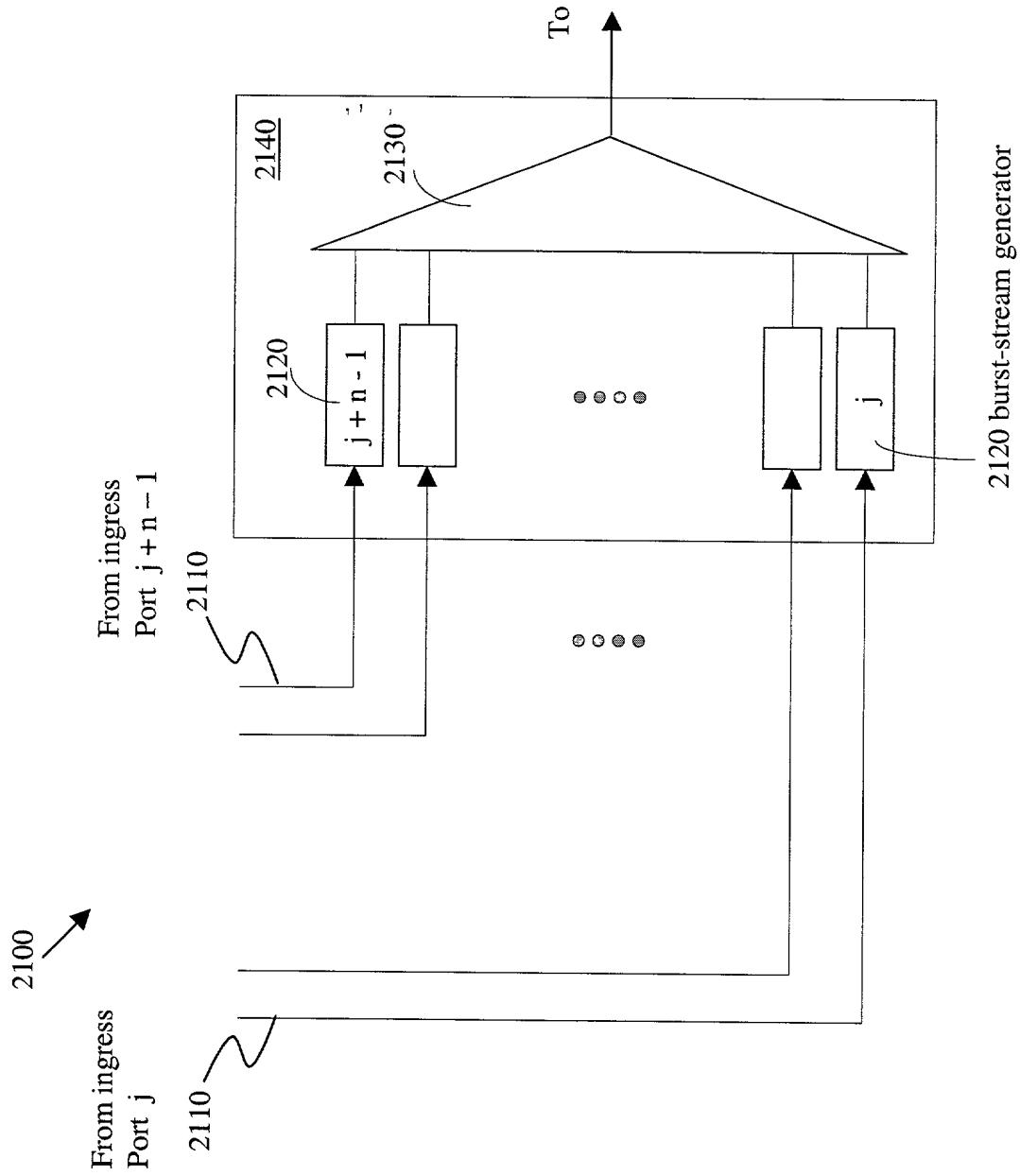
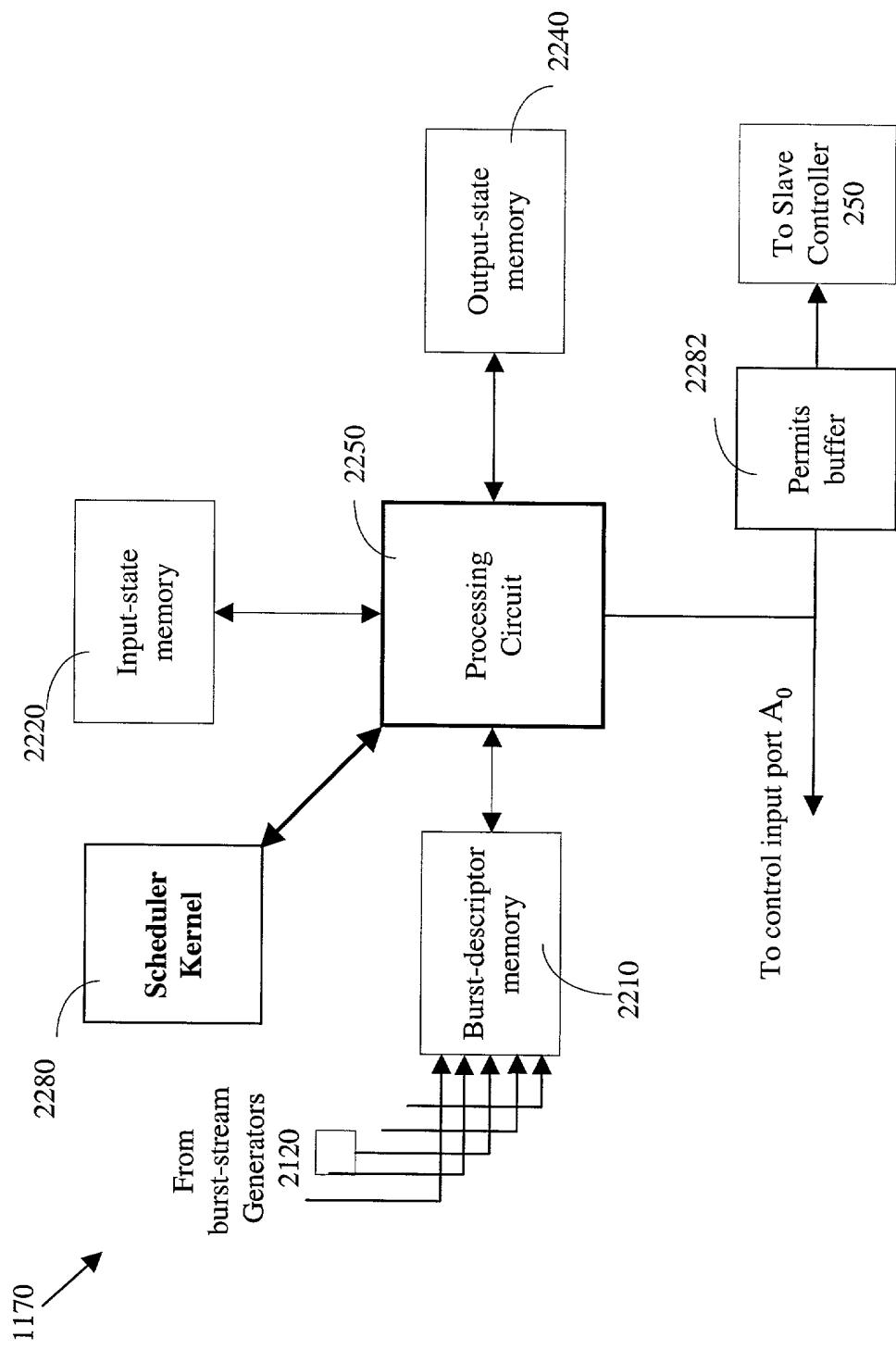


FIG. 21

2120 burst-stream generator

FIG. 22



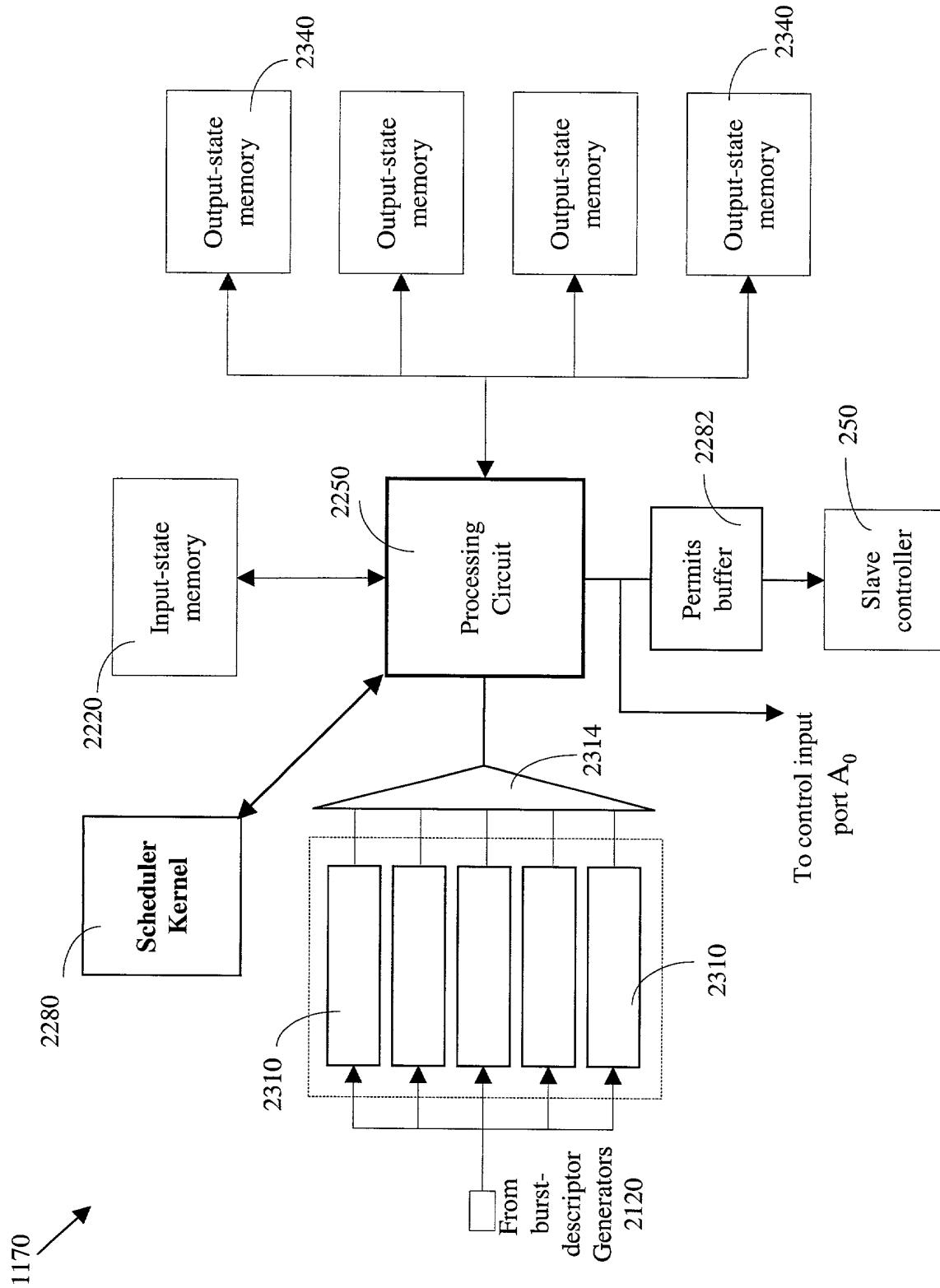


FIG. 23

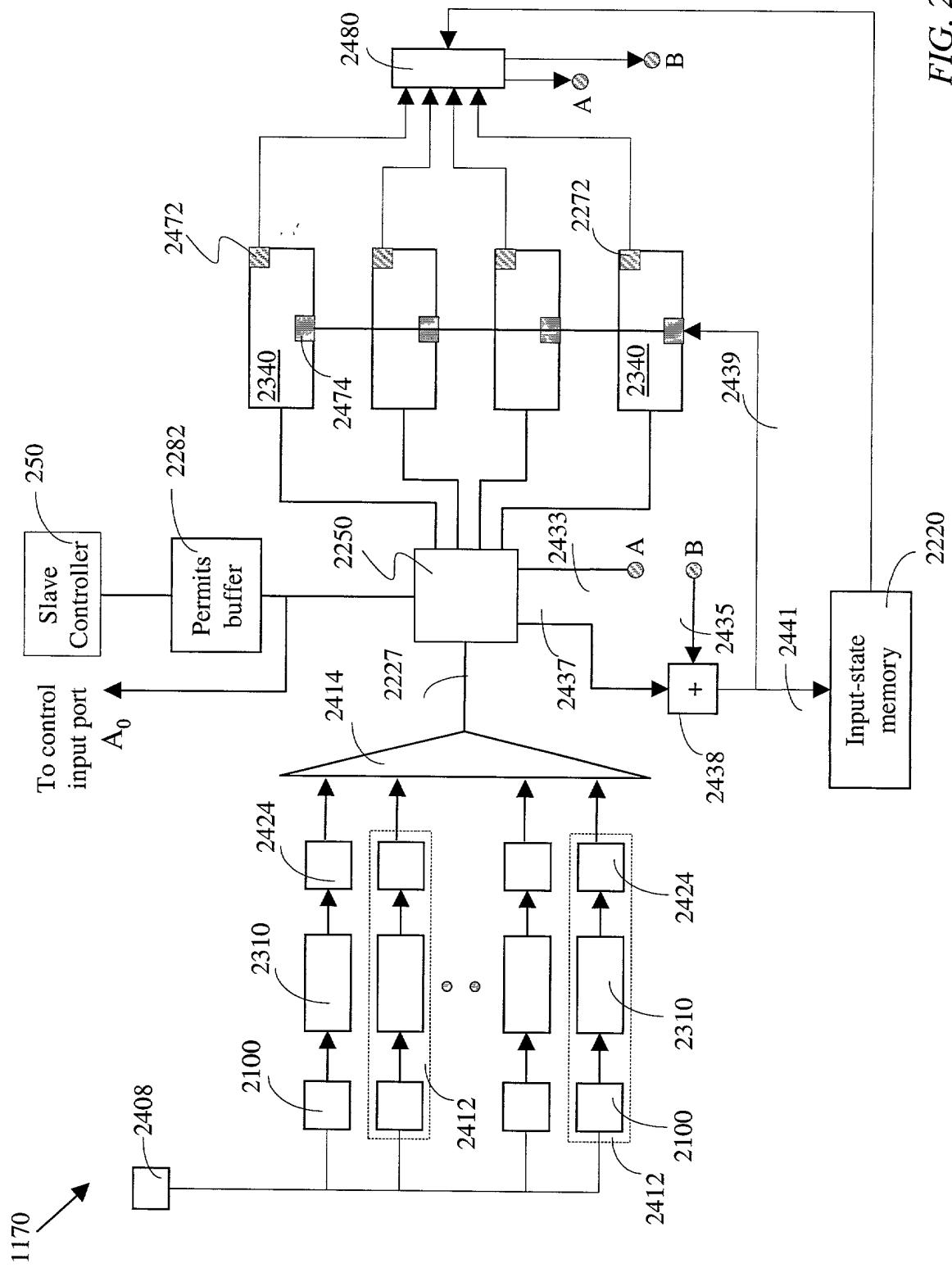


FIG. 24

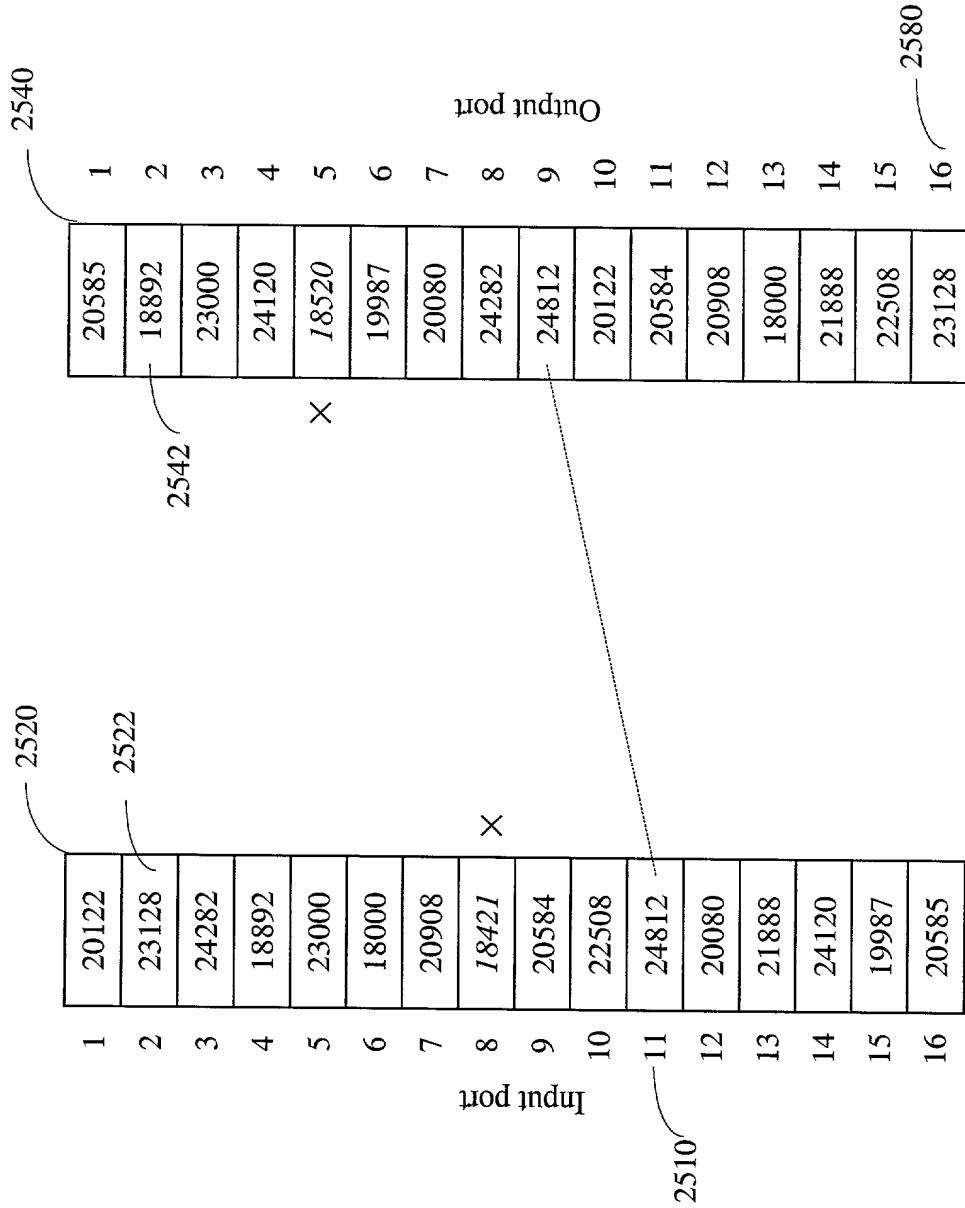


FIG. 25

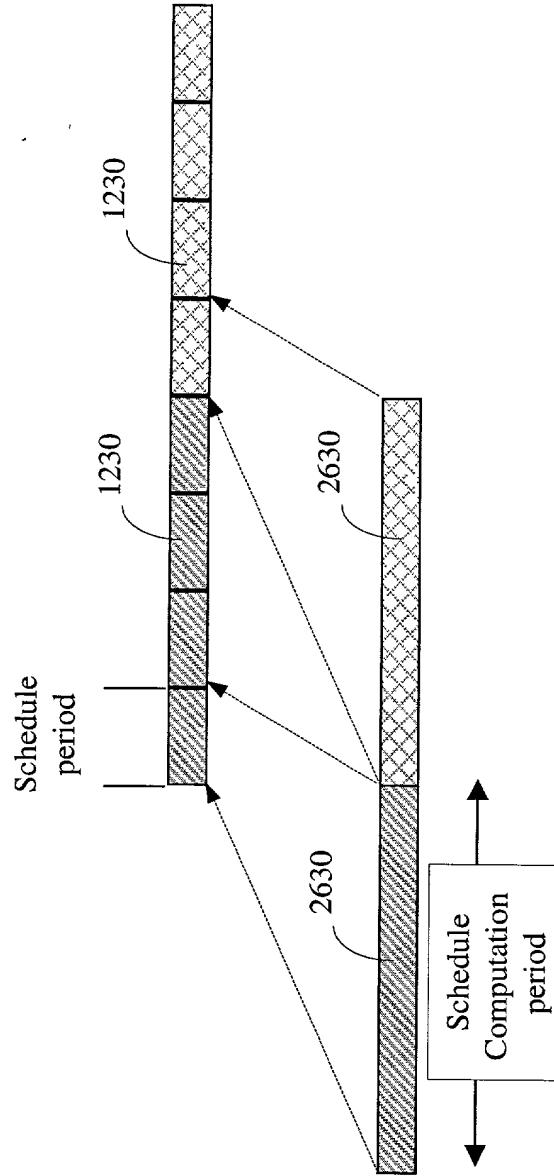


FIG. 26

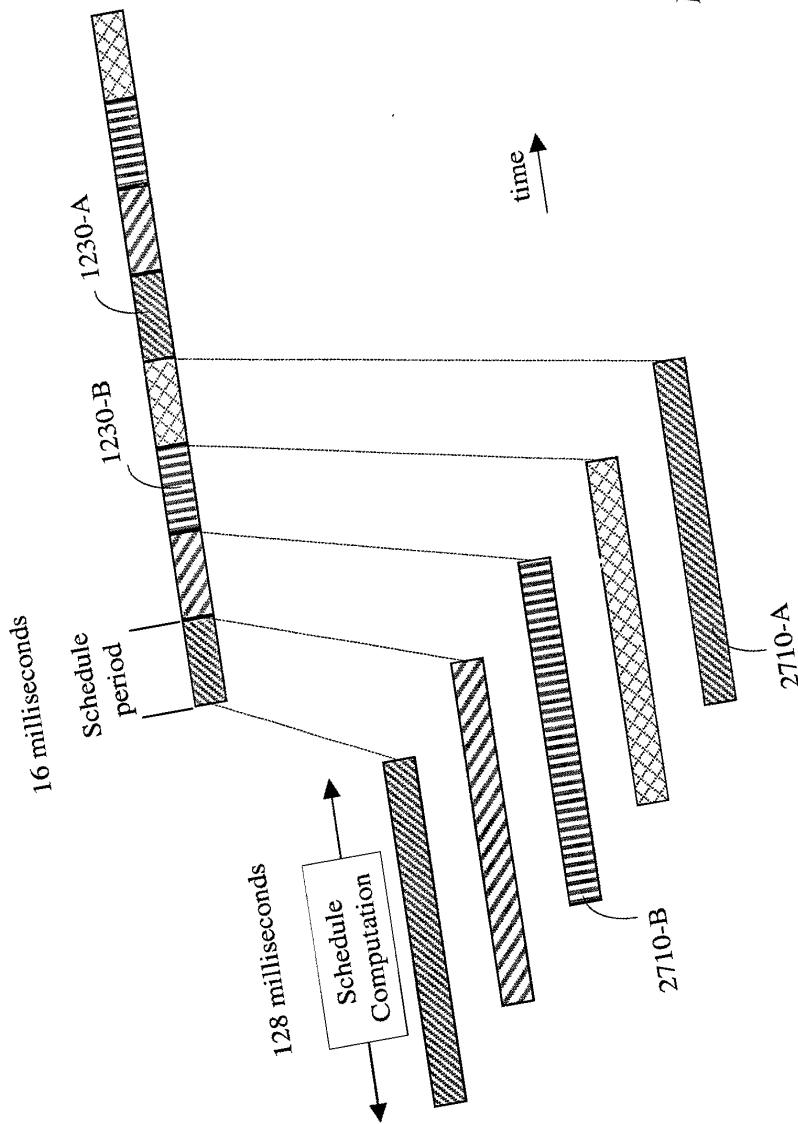


FIG. 27

FIG. 28-a

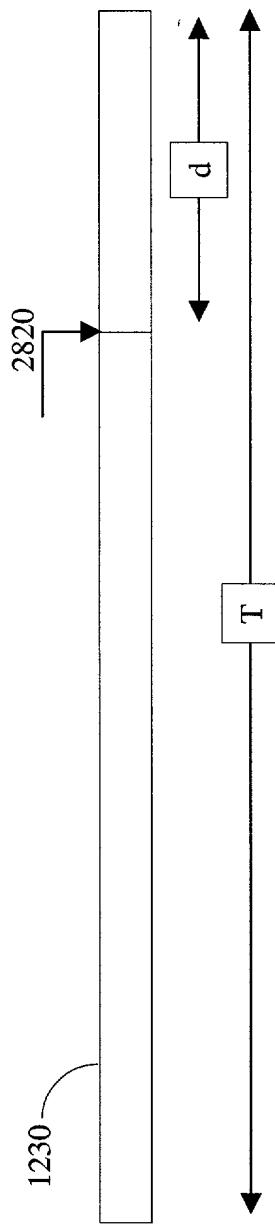


FIG. 28-b

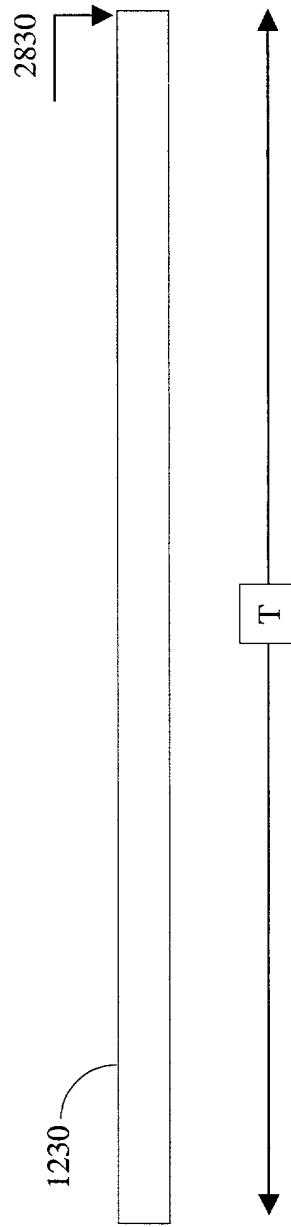


FIG. 28

FIG. 29

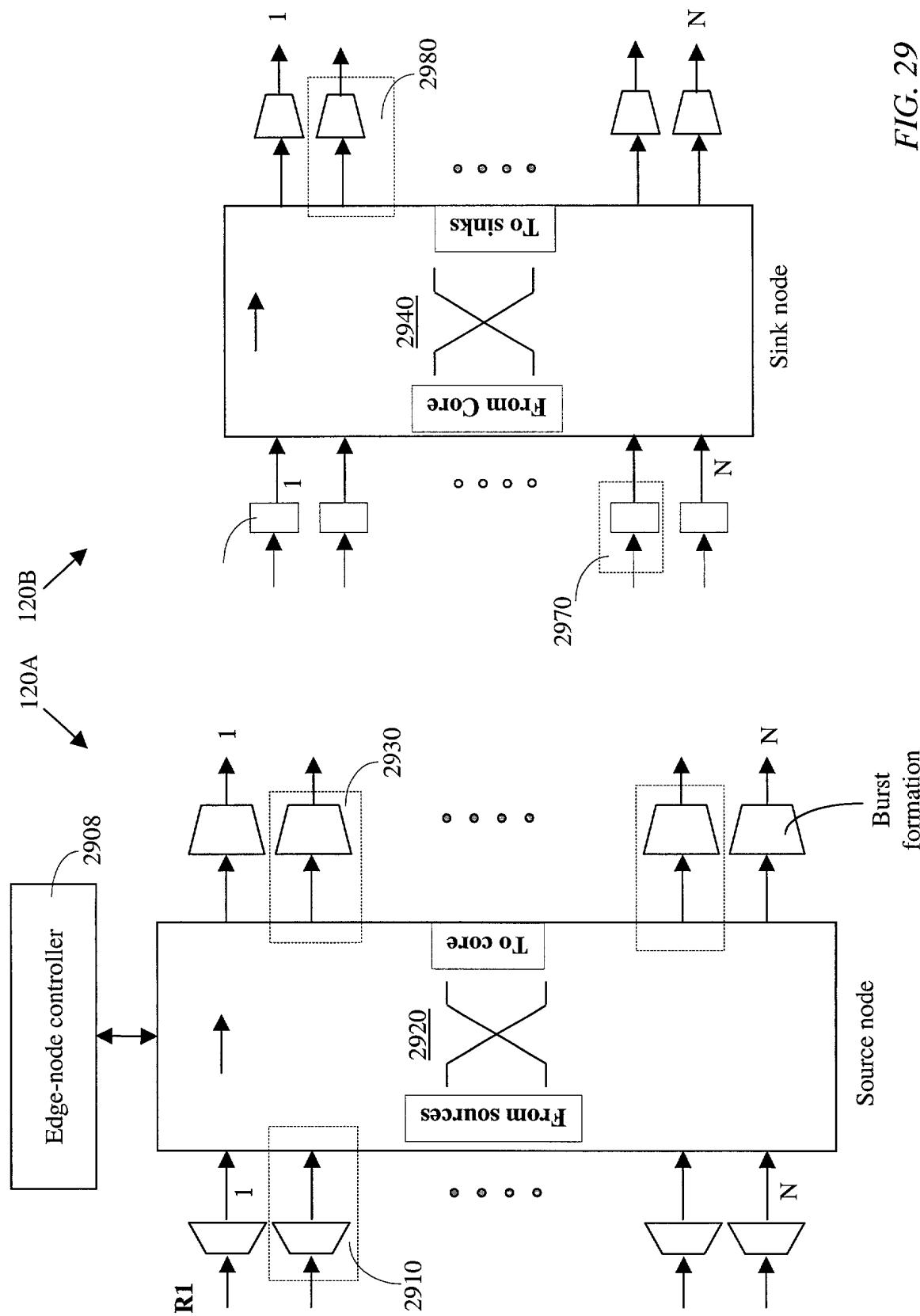


FIG. 30

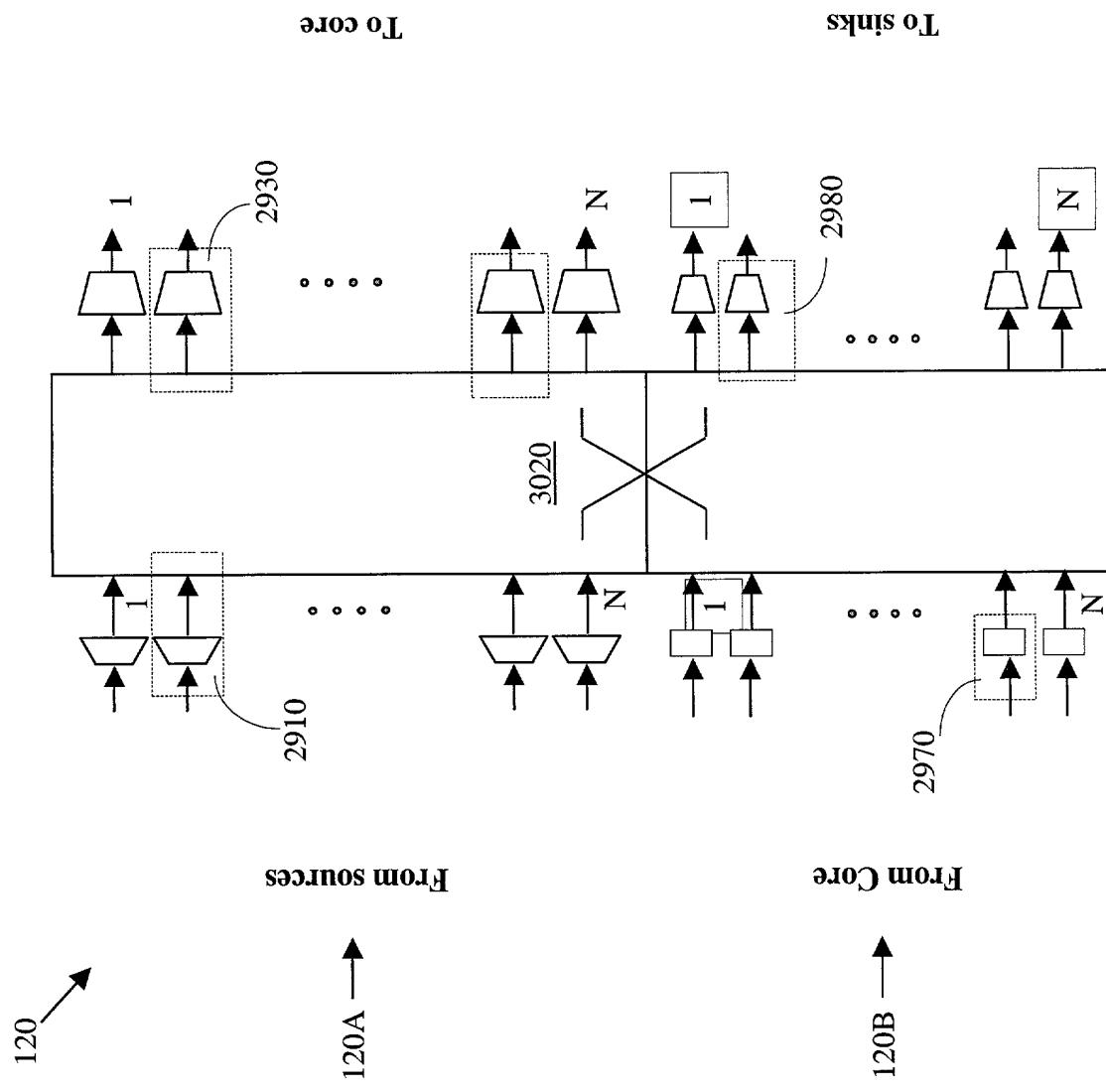
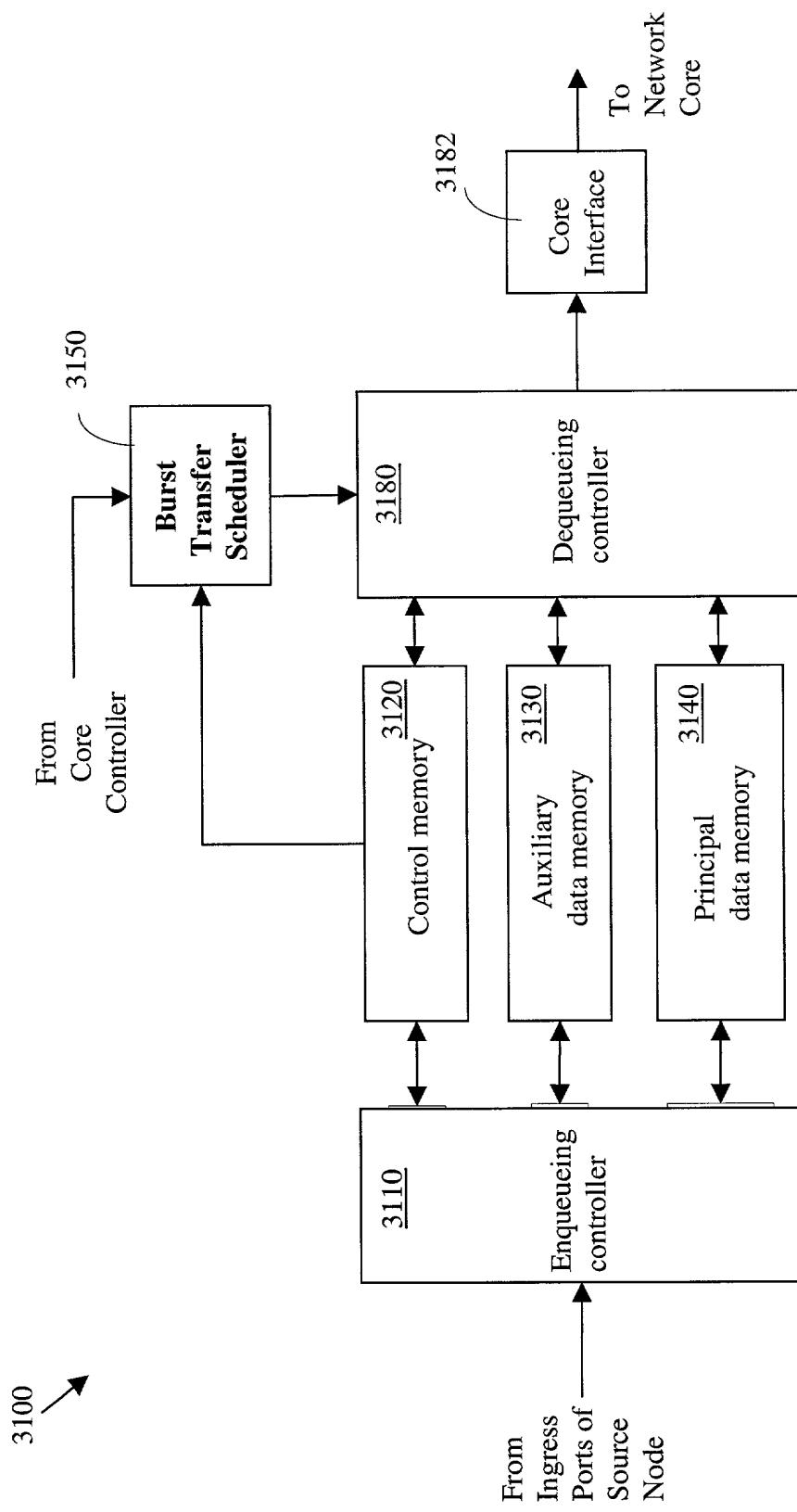


FIG. 31



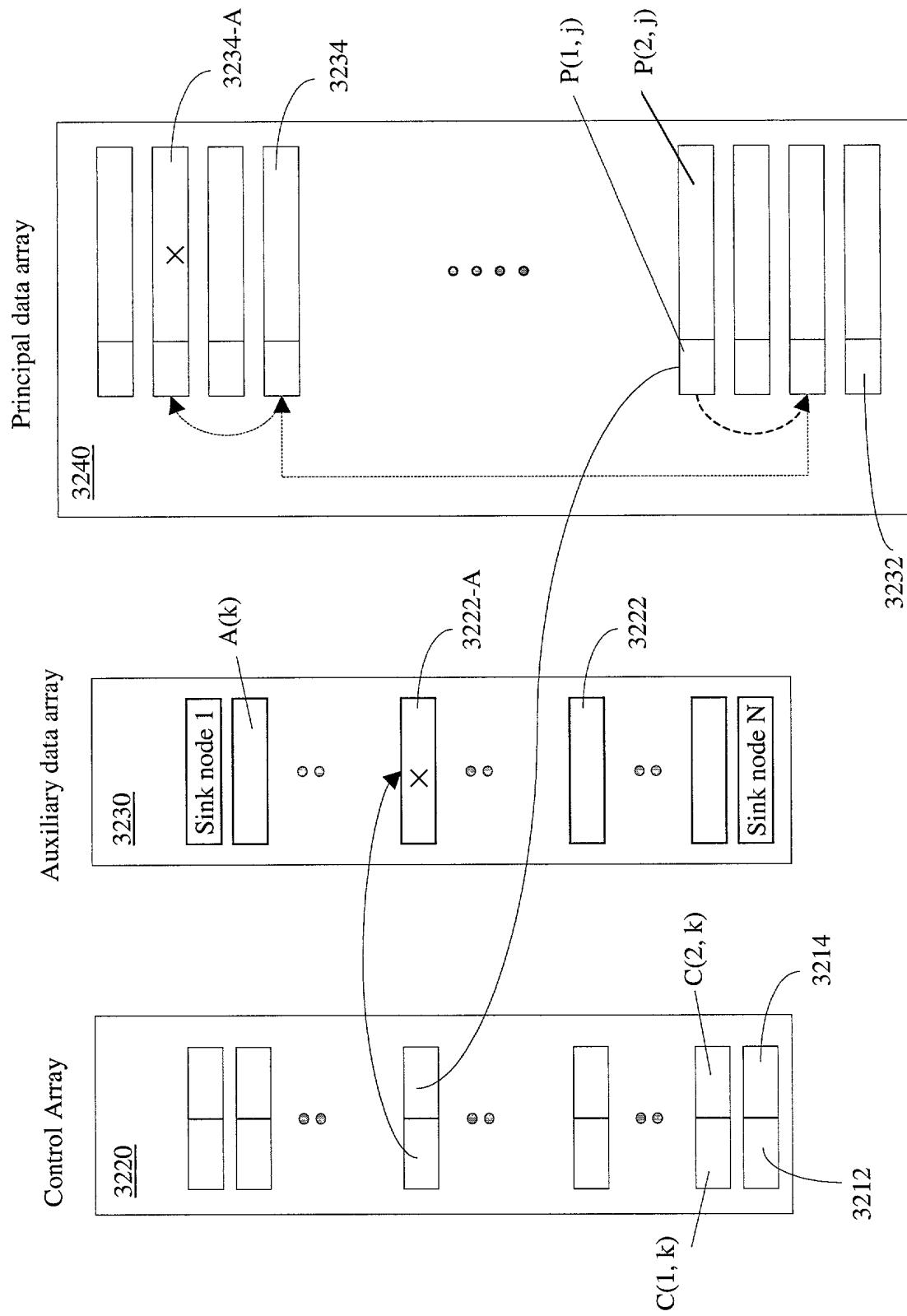


FIG. 32

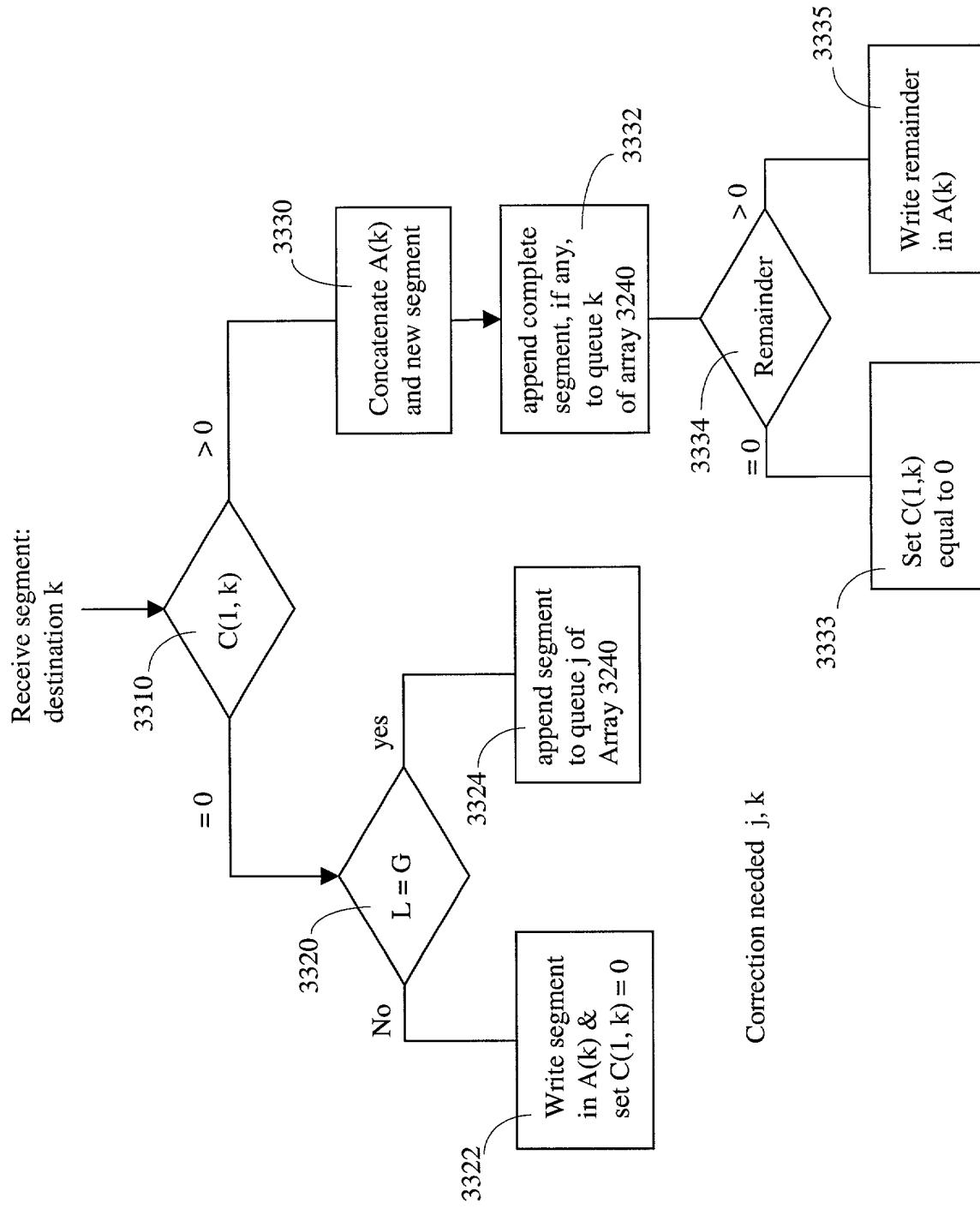


FIG. 33

$$S1 = \begin{cases} 0 & \text{if } C(1, k) = 0 \\ 1 & \text{otherwise} \end{cases} \quad S2 = \begin{cases} 0 & \text{if } C(2, k) = 0 \\ 1 & \text{otherwise} \end{cases}$$

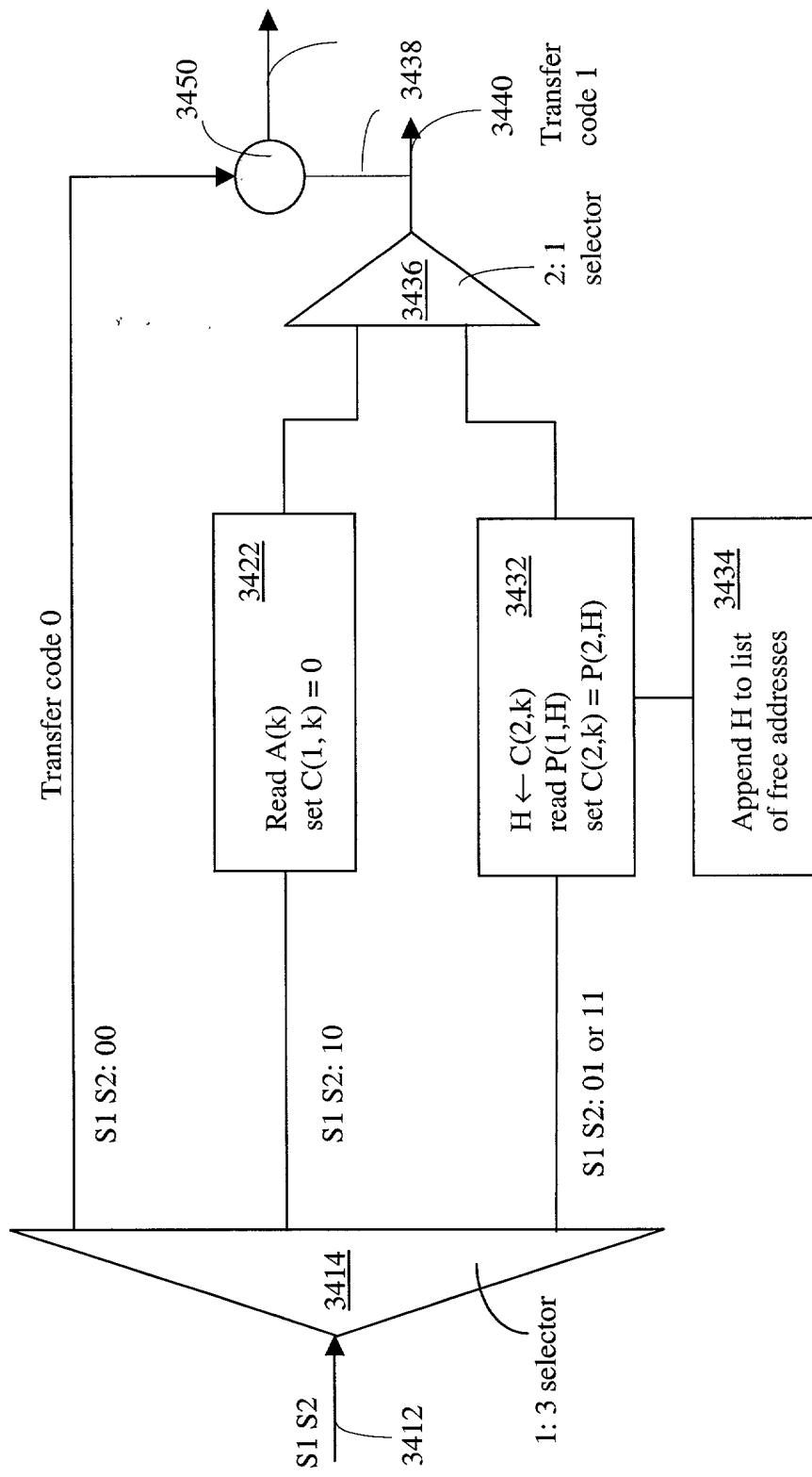
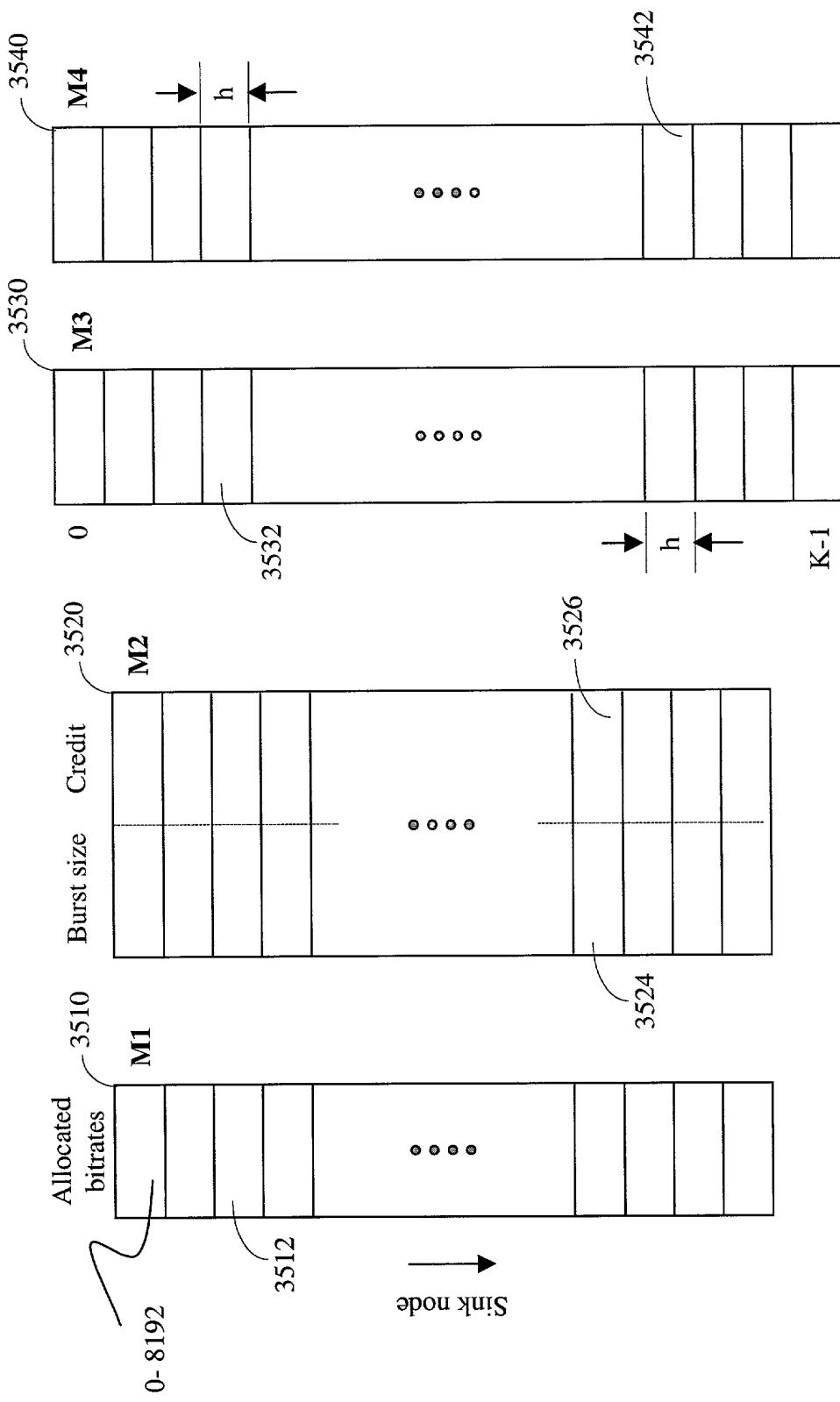


FIG. 34

FIG. 35



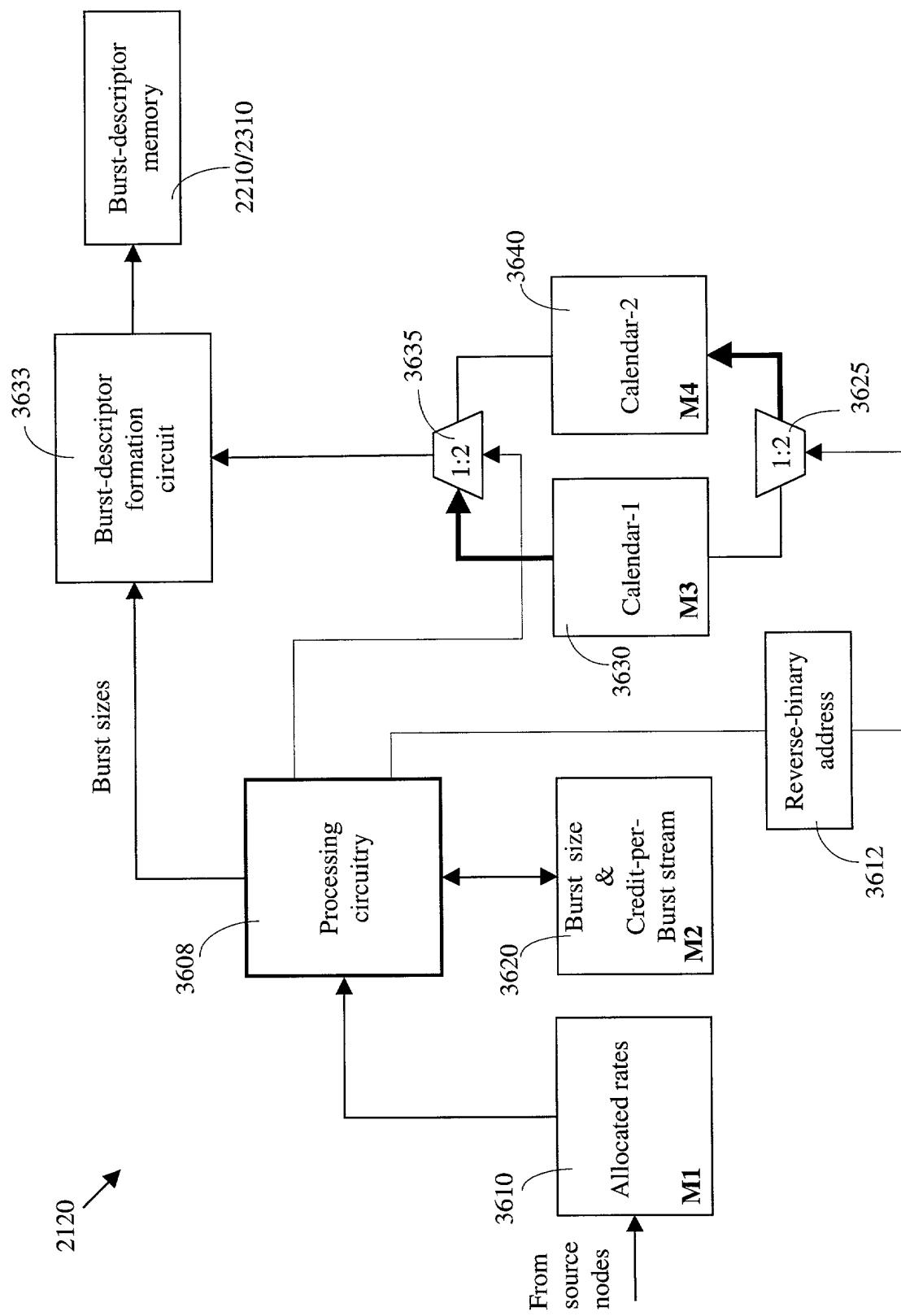


FIG. 36